



Sri Indu Institute of Engineering & Technology

Recognized Under 2(f) of UGC Act 1956

Approved by AICTE, New Delhi

Affiliated to JNTUH, Hyderabad.

1.1.1 The Institution ensures effective curriculum delivery through a well planned and documented process

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PRINCIPAL
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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by Govt. Act No. 30 of 2008)
Kukatpally, Hyderabad – 500085, Telangana, India.

Dr. M. Manzoor Hussain
M.Tech., Ph.D.,
Professor of Mechanical Engineering &
REGISTRAR

Lr. No. DAPO/Acad. Cal./Commencement of on-campus Class work /2021 Dt: 20.03.2021

To
The Directors / Principals of Constituent, Affiliated and Autonomous Colleges of JNTUH

- Sub: JNTUH, Hyderabad – Directorate of Academic & Planning – Commencement of on-campus Class work for the Academic Year 2020-21- Reg.
Ref: 1 UGC guide lines for reopening the universities and colleges post lockdown due to COVID-19 pandemic, dated 05.11.2020.
2 Note orders of the Vice-Chancellor dated 20.03.2021.

With reference to the subject cited above, it is here by informed the Directors / Principals of all constituent, affiliated and autonomous Colleges of JNTUH that on-campus classwork for UG IV-Year II-Semester, I-Year I-Semester and PG I-Semester students studying in various courses shall commence from 22-03-2021 (Monday). You are requested to maintain proper hygienic conditions as per Standard Operating Procedure (SOP) given by UGC for COVID-19 pandemic.

All the Directors / Principals have to make a note that all the COVID-19 pandemic norms prescribed by State Government of Telangana and UGC shall be followed scrupulously to ensure smooth functioning of the Academic and Administrative activities.

The following are the Academic schedules and Guidelines to be followed:

Academic Schedules:


Dates	B. Tech./B. Pharm courses (IV Year II Sem & I Year I Sem)	B. Tech./B. Pharm courses (II & III Years II Sem)	MBA/MCA/M.Tech/ M.Pharm. Courses I Sem and Pharm.D/ Pharm.D(PB)
22-3-2021 to 30-4-2021	On-campus classes	Online classes	On-campus classes

Contd...2

Guidelines

1. All the above students including faculty and supporting staff shall be intimated about the commencement of class work from 22.03.2021. Accordingly, the academic units shall keep the entire premises ready for starting on-campus class work.
2. The Principals are requested to identify the students from outstation who require accommodation in College attached Hostels and arrange them the hostel accommodation by following the COVID-19 precautionary norms.
3. The Colleges are advised to maintain proper hygienic conditions in each and every unit with frequent sanitization of class rooms, laboratories, toilets, etc. The Colleges shall take separate **Declaration forms** from the students that they will follow strictly the COVID-19 precautionary norms in the college premises and hostels.
4. The parents of the students seeking hostel facility shall give an **Undertaking form** that, the hostel accommodation is provided at the request of the parents and the stay of their wards in the hostel is the sole responsibility of both students and parents.
5. It is suggested that, the students shall come with latest RT-PCR test report particularly for the students staying in the hostel.
6. The Colleges are suggested to display boards / flexi banners indicating precautions/procedures for prevention of COVID-19 at all prominent points in the campus and make sure that they are visible to all the students, staff and visitors.
7. It is suggested that every College administration shall be prepared to face any medical emergencies related to COVID-19 /any other case(s) in their campus. Further, all the Colleges are advised to provide the necessary arrangement for the affected person(s) for isolation and medical care.
8. All the faculty, staff and students shall wear masks, use sanitizer frequently. The college shall provide extra masks in each department for the needy people.
9. The Colleges shall make necessary arrangements to provide Hot water to the students by providing Hot Water Dispenser system in the Mess / Dining halls of the Hostel.
10. Necessary equipment shall be procured for thermal scanning of students at least once in a day at the respective departments in the campus and also in mess / hostel premises.
11. Further, any additional measures initiated by the Colleges towards the prevention of COVID-19 and for the welfare of the Students, Faculty members & Supporting staff are highly appreciated.
12. All the faculty members shall attend the College without fail irrespective of whether they are engaging classes online or offline.

Yours sincerely


20/03/2021
REGISTRAR


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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
ACADEMIC CALENDAR 2020-21

For All Constituent & Affiliated Colleges of JNTUH
B. Tech./B.Pharm. I Year I & II Semesters
 (Online Classes)

B. Tech./B.Pharm. I Year - I Semester

S. No	Description	Duration	
		From	To
1	Commencement of I Semester classwork / Orientation Programme		01.12.2020
2	1 st Spell of Instructions	01.12.2020	23.01.2021 (8 Weeks)
3	First Mid Term Examinations	25.01.2021	30.01.2021 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before		06.02.2021
5	Parent-Teacher Meeting		12.02.2021
6	2 nd Spell of Instructions	01.02.2021	27.03.2021 (8 Weeks)
7	Second Mid Term Examinations (including public holidays)	29.03.2021	06.04.2021 (1 Week)
8	Preparation Holidays and Practical Examinations	07.04.2021	12.04.2021 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before		12.04.2021
10	End Semester Examinations	15.04.2021	29.04.2021 (2 Weeks)

B. Tech./ B.Pharm. I Year - II Semester

S. No	Description	Duration	
		From	To
1	Commencement of II Semester classwork		30.04.2021
2	1 st Spell of Instructions	30.04.2021	24.06.2021 (8 Weeks)
3	First Mid Term Examinations	25.06.2021	30.06.2021 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before		05.07.2021
5	Parent-Teacher Meeting		09.07.2021
6	2 nd Spell of Instructions	01.07.2021	25.08.2021 (8 Weeks)
7	Second Mid Term Examinations	26.08.2021	01.09.2021 (1 Week)
8	Preparation Holidays and Practical Examinations	02.09.2021	08.09.2021 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before		08.09.2021
10	End Semester Examinations	09.09.2021	22.09.2021 (2 Weeks)

Note: All the laboratory courses shall be conducted once normalcy is restored.


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M.Tech., Ph.D.,
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Lr. No. A1/4/revised Academic calendars/2021

Date: 18.05.2021

To,
The Principals of Constituent & Affiliated Colleges of JNTUH,
Engineering and Pharmacy colleges

Sir/Madam,

Sub: JNTUH-Academic & Planning, revised academic calendar for II and III-year II semesters of B.Tech./B.Pharm, MBA/MCA II Year II Semester and Pharm. D (Regular) II, III, IV, V Year and Pharm.D (PB) II Year for the academic year 2020-21.
Ref: 1. Note orders of the Vice-Chancellor, dated: 18-5-2021.

With reference to the representations received due to unforeseen prevailing COVID-19 conditions and lockdown (12-5-2021 to 22-5-2021), after review revised the revised academic calendar for B. Tech./ B.Pharm. II & III Year - II Semester, MBA/MCA II Year II Semester and Pharm. D (Regular) II, III, IV, V Year and Pharm D (PB) II Year is given below. The summer vacation of 2 weeks from 17-5-2021 to 29-5-2021 is added to preparation/laboratory class work/lab exams.

The Principals of Constituent & Affiliated Colleges of JNTUH are requested to take necessary measures and instruct the HODs/staff members to follow the revised schedule. Please feel free to clarify any doubt you have in this connection. This is for your reference and necessary action.

Yours Sincerely


REGISTRAR
18/05/21

Encl: Revised Academic Calendar

1. B. Tech./ B.Pharm. II & III Years - II Semester
2. MBA/MCA II Year II Semester
3. Pharm. D (Regular) II, III, IV, V Year and Pharm.D (PB) II Year

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

Revised Academic Calendar 2020-21

For All Constituent & Affiliated Colleges of JNTUH

B. Tech./ B.Pharm. II & III Years - II Semester

S. No	Description	Duration	
		From	To
1	Commencement of II Semester classwork	22.03.2021	
2	1 st Spell of Instructions	22.03.2021	15.05.2021 (8 Weeks)
3	2 nd Spell of Instructions	17.05.2021	29.05.2021 (2 Weeks)
4	First Mid Term Examinations	31.05.2021	05.06.2021 (1 Week)
5	Submission of I-Mid Term Exam Marks to the University on or before	11.06.2021	
6	Continuation of 2 nd Spell of Instructions	07.06.2021	17.07.2021 (6 Weeks)
7	Second Mid Term Examinations	19.07.2021	24.07.2021 (1 Week)
8	Preparation Holidays / Conduct of laboratory classes / Practical Examinations	26-7-2021	14.08.2021 (3 Weeks)
10	Submission of II-Mid Term Exam Marks to the University on or before	04.08.2021	
11	End Semester Examinations	16.08.2021	28.08.2021 (2 Weeks)


REGISTRAR
18/08/21

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Lr. No. A1/4/revise Academic calendar/IV UG/2021

Date: 05.06.2021

To,
The Principals of Constituent & Affiliated Colleges of JNTUH.

Sir/Madam,

Sub: JNT University Hyderabad – Academic & Planning – revised academic calendar for IV-II semesters of B.Tech./B. Pharm for the academic year 2020-21.

Ref: I. Note orders of the Vice-Chancellor, Dt: 5.6.2021.

With reference to the lockdown period (12-5-2021 to 22-5-2021) and lockdown extension till 09-06-2021, a revised academic calendar for B. Tech/ B. Pharm. IV Year - II Semester is rescheduled. The Principals of Constituent & Affiliated Colleges of JNTUH are requested to make a note and instruct the faculty to follow accordingly.

This is for your reference and necessary action.

Yours Sincerely,


05/06/21
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Encl: Revised Academic Calendar


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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
Revised Academic Calendar 2020-21
For All Constituent & Affiliated Colleges of JNTUH

B. Tech/ B.Pharm. IV Year - II Semester

S. No	Description	Duration	
		From	To
1	Commencement of II Semester classwork	22.03.2021	
2	1 st Spell of Instructions including First Mid Term Examinations (3 Subjects only)	22.03.2021	01.05.2021 (6Weeks)
3	Submission of First Mid Term Exam Marks to the University on or before	05.05.2021	
4	2 nd spell instructions including Second mid-term examinations	03-05-2021	26-06-2021 (8 weeks)
6	Project-work Viva-voce exams and preparation holidays	28-06-2021	30-06-2021
7	Submission of second mid-term exam marks to the University and upload of project-viva exam award lists on or before	02-07-2021	
8	Commencement of IV-II semester examinations	01-07-2021	07-07-2021

Note: Total No. of days: 90 days including Saturdays/Sundays and excluding public holidays. The colleges are informed to increase the instruction hours per week such that the AICTE specified instruction hours per subject are fully complied with, i.e., 32 hours for each two-credit theory course and 48 hours for each three-credit theory course in IV-II semester. Similar requirement for project work should also be complied with. Further the colleges are informed to upload the details of class-work, i.e. number of hours of classes taken per each subject till 31-05-2021 to the examination branch portal in the excel template given by the examination branch.


 25/06/21
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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
Revised Academic Calendar for 2020-21
B. Tech./ B.Pharm. I Year - II Semesters

Revised Academic Calendar for B. Tech. I Year - II Semester for the Academic Year 2020-21

S. No	Description	Duration	
		From	To
1	Commencement of II Semester classwork	15.04.2021	
2	1 st Spell of Instructions	15.04.2021	09.06.2021 (8 Weeks)
3	First Mid Term Examinations	10.06.2021	16.06.2021 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before	23.06.2021	
5	2 nd Spell of Instructions including End Semester Examinations for I / I Sem Lab Classes & Lab Examinations for I / I Sem	17.06.2021	28.08.2021 (8 Weeks + Examinations+ Labs)
		05.07.2021	13.07.2021
		16.08.2021	24.08.2021 (1 Week)
6	Second Mid Term Examinations	30.08.2021	04.09.2021 (1 Week)
7	Preparation Holidays, Lab Classes & Lab Examinations	06.09.2021	18.09.2021 (2 Weeks)
8	Submission of Second Mid Term Exam Marks to the University on or before	13.09.2021	
9	End Semester Examinations	20.09.2021	01.10.2021 (2 Weeks)

Revised Academic Calendar for B. Pharm. I Year - II Semester for the Academic Year 2020-21

S. No	Description	Duration	
		From	To
1	Commencement of II Semester classwork	15.04.2021	
2	1 st Spell of Instructions	15.04.2021	09.06.2021 (8 Weeks)
3	First Mid Term Examinations	10.06.2021	16.06.2021 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before	23.06.2021	
5	2 nd Spell of Instructions including End Semester Examinations for I / I Sem Lab Classes & Lab Examinations for I / I Sem	17.06.2021	28.08.2021 (8 Weeks + Examinations+ Labs)
		05.07.2021	15.07.2021 & 22.07.2021
		16.08.2021	24.08.2021 (1 Week)
6	Second Mid Term Examinations	30.08.2021	04.09.2021 (1 Week)
7	Preparation Holidays, Lab Classes & Lab Examinations	06.09.2021	18.09.2021 (2 Weeks)
8	Submission of Second Mid Term Exam Marks to the University on or before	13.09.2021	
9	End Semester Examinations	20.09.2021	01.10.2021 (2 Weeks)


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Dr. M. Manzoor Hussain
M.Tech., Ph.D.,
Professor of Mechanical Engineering &
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Circular No. A1/Lab Classes & Examinations /2020-21,

Date: 27.07.2021

CIRCULAR

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It is hereby informed to the Directors/ Principals of Constituent, Autonomous and Affiliated College of JNTU II to conduct the Lab classes and Lab Examinations by offline as per schedule mentioned in the Academic Calendar for B.Tech/ B.Pharm., M.Tech/M.Pharm., MBA, MCA and Pharm.D. courses for the academic 2020-21.

Lab Classes and Lab Examinations:

Courses	Description	Duration	
		From	To
B.Tech / B.Pharm. I Year I Sem.	Lab Classes & Lab Examinations (as per revised calendar)	16.08.2021	24.08.2021 (1 Week)
B.Tech / B.Pharm. I Year II Sem.	Preparation Holidays, Lab Classes & Lab Examinations (as per revised calendar)	06.09.2021	18.09.2021 (2 Weeks)
B.Tech / B.Pharm. II & III Year II Sem	Preparation Holidays, Lab Classes & Lab Examinations (as per schedule/ as per the college convenience excluding the dates 3 rd , 4 th , 5 th , 6 th , 9 th & 10 th of Aug. 2021 for the colleges which have TS ECET-2021/TS EAMCET-2021 Test Centres)	26.07.2021	14.08.2021 (3 Weeks)
M.Tech / M.Pharm./ MBA/ MCA- I Year I Sem.	Preparation Holidays, Lab Classes & Lab Examinations (middle of the exams of I/I Sem/ as per the college convenience)	03.08.2021	14.08.2021 (10 Days)
M.Tech / M.Pharm / MBA/ MCA- I Year II Sem.	Preparation Holidays, Lab Classes & Lab Examinations (as per revised calendar)	30.09.2021	13.10.2021 (2 Weeks)


MBA/MCA- II Year II Sem.	Practical classes & examinations to be conducted as per the convenience after End Semester Examinations (i.e. after 25.8.2021, if not conducted) & submission of practical exam marks to the University.		
Pharm.D & Pharm.D (PB) - I Year	Preparation Holidays & Lab Examinations (as per schedule)	13.09.2021	25.09.2021 (2 Weeks)
Pharm.D- II, III, IV, V Years & Pharm.D (PB) II Year	Practical classes & examinations to be conducted as per the convenience after End Examinations (if not conducted) & submission of practical exam marks to the University.		

The Directors /Principals of Constituent, Autonomous and Affiliated Colleges are requested to take necessary action above Lab Classes and Examinations and follow the Covid precautions.

Revised Academic Calendars for B. Tech. / B.Pharm. I Year - II Sems. & M.Tech./ M.Pharm. /MBA / MCA-I Year - II Sems. for the academic 2020-21 are enclosed.


 REGISTERED
 30/7/21

To,
 All the Directors /Principals of Constituent, Autonomous and Affiliated Colleges of JNTUH.
 Copy to the DE/PA to Rector/ PA to Vice-Chancellor, JNTUH, Hyderabad.


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Sri Indu Institute of Engineering & Technology

Recognized Under 2(f) of UGC Act 1956

Approved by AICTE, New Delhi

Affiliated to JNTUH, Hyderabad.

Vision

To become a premier institute of academic excellence by providing the world class education that transforms individuals into high intellectuals, by evolving them as empathetic and responsible citizens through continuous improvement.

Mission

- To offer outcome-based education and enhancement of technical and practical skills.
- To continuously assess teaching-learning process through institute-industry collaboration.
- To be a centre of excellence for innovative and emerging fields in technology development with state-of-art facilities to faculty and students' fraternity.
- To Create an enterprising environment to ensure culture, ethics and social responsibility among the stakeholders.

Quality Policy

- To ensure students to uphold moral and ethical values.
- To expose the students to understand the socio-economic strata of the society with an empathetic attitude.
- To nurture talent and entrepreneurship and enable all-round development in students.
- To cater to the demand-driven needs of various stake holders. To continually improve all the processes through endorsing cognizance, conducting periodical reviews and unifying trainings at all levels.



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Institution academic calendar- 2020-21

B.Tech II, III, IV Years - I Semester

S.No	Event	Date	Duration
1	Commencement of Instructions	01-09-2020	-----
2	1 st Spell of instructions (including Dussehra Recess)	01-09-2020 To 31-10-2020	9 weeks
3	Dussehra Recess	19-10-2020 To 24-10-2020	6Days
4	End examinations preparation holiday-previous semesters	02-11-2020 To 04-11-2020	3Days
5	2 nd Spell of instructions (Including First Mid Term Examination)	14-12-2020 To 13-02-2021	9 weeks
6	1 st Mid Term Examinations	21-12-2020 To 28-12-2020	1 week
7	Submission of first mid term exam marks to university on or before	04-01-2021	-----
8	Parents Teachers Meeting	08-01-2021	1 Day
9	2 nd Mid Term Examinations	15-02-2021 To 20-02-2021	1 week
10	Practical classes	22-02-2021 To 27-02-2021	1 week
11	Preparation holidays & practical examinations	01-03-2021 To 06-03-2021	1 week
12	Submission of second mid term exam marks to university on or before	27-02-2021	-----
13	End semester examination	08-03-2021 To 20-03-2021	2 weeks



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Institution academic calendar- 2020-21

B.Tech II, III, IV Years - II Semester

S.No	Event	Date	Duration
1	Commencement of II semester class work	22-03-2021	-----
2	1 st Spell of instructions	22-03-2021 To 15-05-2021	8 weeks
3	Summer vacation	17-05-2021 To 29-05-2021	2 weeks
4	1 st Mid Term Examinations	31-05-2021 To 05-06-2021	1 week
5	Submission of first mid term exam marks to university on or before	11-06-2021	-----
6	2 nd Spell of instructions	07-06-2021 To 31-07-2021	8 weeks
7	2 nd Mid Term Examinations	02-08-2021 To 07-08-2021	1 week
8	Preparation holidays & practical examinations	09-08-2021 To 14-08-2021	1 week
9	Submission of second mid term exam marks to university on or before	14-08-2021	-----
10	End semester examination	16-08-2021 To 28-08-2021	2 weeks



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Institution academic calendar- 2020-21

B.Tech IV Years - II Semester

(Revised Academic Calendar)

S.No	Event	Date	Duration
1	Commencement of II semester class work	22-03-2021	-----
2	1 st Spell of instructions including first mid term examination (3 subjects only)	22-03-2021 To 01-05-2021	6 Weeks
3	Submission of first mid term exam marks to university on or before	05-05-2021	-----
4	2 nd Spell of instructions including second mid term examination	03-05-2021 To 26-06-2021	8 Weeks
5	Project-work viva-voce exams and preparation holidays	28-06-2021 To 30-06-2021	3 Days
6	Submission of second mid term exam marks to university and upload of project-viva exam award list on or before	2-07-2021	-----
7	End semester examination	01-07-2021 To 07-07-2021	1 Week



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SRI INDU INSTITUTE OF ENGINEERING AND TECHNOLOGY
DEPARTMENT OF CIVIL ENGINEERING

Department Vision:

To become a school of excellence that brings out civil engineers with high technical competencies and promotes high-end research to meet the current and future challenges in civil engineering.

Department Mission:

DM1: To produce civil engineers of high caliber, technical skills and ethical values to serve the society and nation.

DM2: By providing infrastructure and state-of-the-art laboratory facilities to meet the requirements of curriculum and research and consultancy services

DM3: To develop strong linkage with relevant industries, institutes of higher learning and reputed research organizations.

DM4: To encourage students to pursue higher education and take competitive exams and various career enhancing courses.

Head of Department
Civil Engg. Dept.
SRI INDU INSTITUTE OF ENGG & TECH
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Department of Civil Engineering

Academic calendar- 2020-21

B.Tech II, III, IV Years - I Semester

S.No	Event	Date	Duration
1	Commencement of Instructions	01-09-2020	-----
2	1 st Spell of instructions (including Dussehra Recess)	01-09-2020 To 31-10-2020	9 weeks
3	Dussehra Recess	19-10-2020 To 24-10-2020	6Days
4	End examinations preparation holiday-previous semesters	02-11-2020 To 04-11-2020	3Days
5	2 nd Spell of instructions (Including First Mid Term Examination)	14-12-2020 To 13-02-2021	9 weeks
6	Add on course webinar on 'AUTO CAAD'	14-12-2020 To 19-12-2020	6 Days
7	1 st Mid Term Examinations	21-12-2020 To 28-12-2020	1 week
8	Submission of first mid term exam marks to university on or before	04-01-2021	-----
9	Parents Teachers Meeting	08-01-2021	1 Day
10	2 nd Mid Term Examinations	15-02-2021 To 20-02-2021	1 week
11	Practical classes	22-02-2021 To 27-02-2021	1 week
12	Preparation holidays & practical examinations	01-03-2021 To 06-03-2021	1 week
13	Submission of second mid term exam marks to university on or before	27-02-2021	-----
14	End semester examination	08-03-2021 To 20-03-2021	2 weeks


HOD
Head of Department
Civil Engg Dept.
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Sri Indu Institute of Engineering & Technology

Recognized Under 2(f) of U.G.C Act 1956

Approved by AICTE, New Delhi Affiliated to JNTUH, Hyderabad.

Main Road, Sheriguda, Ibrahimpatnam, R.R. Dist. 501 510.

Campus Ph: 9640590999, 9347187999, 8096951507.

<https://sliet.ac.in>

Department of Civil Engineering

Academic calendar- 2020-21

B.Tech II, III, IV Years - II Semester

S.No	Event	Date	Duration
1	Commencement of II semester class work	22-03-2021	-----
2	1 st Spell of instructions	22-03-2021 To 15-05-2021	8 weeks
3	Summer vacation	17-05-2021 To 29-05-2021	2 weeks
4	1 st Mid Term Examinations	31-05-2021 To 05-06-2021	1 week
5	Submission of first mid term exam marks to university on or before	11-06-2021	-----
6	2 nd Spell of instructions	07-06-2021 To 31-07-2021	8 weeks
7	Add on course webinar on 'Structural design using STAAD.Pro'	05-07-2021 To 12-07-2021	8 Days
8	2 nd Mid Term Examinations	02-08-2021 To 07-08-2021	1 week
9	Preparation holidays & practical examinations	09-08-2021 To 14-08-2021	1 week
10	Submission of second mid term exam marks to university on or before	14-08-2021	-----
11	End semester examination	16-08-2021 To 28-08-2021	2 weeks


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Head of Department
Civil Engg Dept
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Department of Civil Engineering

Academic calendar- 2020-21

B.Tech IV Years - II Semester

(Revised Academic Calendar)

S.No	Event	Date	Duration
1	Commencement of II semester class work	22-03-2021	-----
2	1 st Spell of instructions including first mid term examination (3 subjects only)	22-03-2021 To 01-05-2021	6 Weeks
3	Submission of first mid term exam marks to university on or before	05-05-2021	-----
4	2 nd Spell of instructions including second mid term examination	03-05-2021 To 26-06-2021	8Weeks
5	Project-work viva-voce exams and preparation holidays	28-06-2021 To 30-06-2021	3 Days
6	Submission of second mid term exam marks to university and upload of project-viva exam award list on or before	2-07-2021	-----
7	End semester examination	01-07-2021 To 07-07-2021	1 Week


HOD Department

Civil Engg Dept.

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Department of Humanities & Sciences

Academic calendar for 2020-21

B. Tech I Year – I Semester

S.No	Event	Date	Duration
1	Commencement Of I Semester Class Work /Orientation Program	01-12-2020	1 day
2	1st Spell Of Instructions:	01-12-2020 to 23-01-2021	-----
3	1st Mid Term Examinations :	25-01-2021 to 30-01-2021	2 weeks
4	Submission Of 1st Mid Term Exam Marks To University On Or Before	06-02-2021	-----
5	Parents Teachers Meeting	12-02-2021	-----
6	2nd Spell Of Instructions	01-02-2021 to 23-03-2021	-----
7	2nd Mid Term Examinations	29-03-2021 to 06-04-2021	-----
8	Preparation Holidays & Practical Examinations	07-04-2021 to 12-01-2021	-----
9	Submission Of 2nd Mid Term Exam Marks To University On Or Before :	12-04-2021	-----
10	End Semester Examinations	15-04-2021 to 29-04-2021	-----

HOD

R. YADAGIRI RAO

M.Sc., B.Ed., M.Tech (CSE)
Head of the Department
Department of Humanities & Sciences
Sri Indu Institute of Engineering & Technology
Sheriguda, Ibrahimpatnam, R.R. Dist. 501 510

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Department of Humanities & Sciences

Academic Calendar- 2020-21

B.Tech I Years - II Semester

S.No	Event	Date	Duration
1	Commencement of II semester class work	15-04-2021	-----
2	1 st Spell of instructions	15-04-2021 To 09-06-2021	8 weeks
3	1 st Mid Term Examinations	10-06-2021 To 16-06-2021	1 week
4	Submission of first mid term exam marks to university on or before	23-06-2021	-----
5	2 nd Spell of instructions &	17-06-2021 To 12-08-2021	8 weeks
6	End semester examinations for I/I semester	05-07-2021 To 13-07-2021	9 Days
7	Lab classes and lab examinations for I/I semester	16-08-2021 To 24-08-2021	1 week
8	2 nd Mid Term Examinations	30-08-2021 To 04-09-2021	1 week
9	Preparation holidays & practical examinations	06-09-2021 To 18-09-2021	2 weeks
10	Submission of second mid term exam marks to university on or before	13-09-2021	-----
11	End semester examination	20-09-2021 To 01-10-2021	2 weeks

HOD


R. YADAGIRI RAO
M.Sc., B.Ed., M.Tech (CSE)
Head of the Department
Department of H&S
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Sheriguda(V), Ibrahimpatnam(R), R.R. Dist-501 510


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R.R. Dist. Telangana -501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-A

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	LTNM	A&DC	ECA	COUN	L U N C H	ECA	EMF&W	LTNM
TUE	ECA	A&DC LAB / ICA LAB				A&DC(T)/LTNM(T)	ECA	LTNM
WED	EMF&W	A&DC	ECA	LTNM		ECA	GS LAB / ECA LAB	
THU	EMF&W	ECA	ECA	EMF&W		SEM	LTNM	A&DC
FRI	A&DC	ECA	ECA LAB / GS LAB			LTNM	EMF&W	ECA
SAT	ECA	LTNM(T)/A&DC(T)	EMF&W	A&DC		ICA LAB / A&DC LAB		

* (1) Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
MA401BS	LTNM-Laplace Transforms, Numerical Methods & Complex Variables	Thirupathi Reddy	EC407PC	ICA Lab-IC Applications Lab.	T.Sadhana/I.Venu
EC402PC	EMF&W-Electromagnetic Fields and Waves	G.Narasimha	EC408PC	ECA Lab-Electronic Circuit Analysis Lab	K.Bhaskar/Dr.S.Suresh
EC403PC	A&DC-Analog and Digital Communications	T.Naresht	*MC409	GS Lab-Gender Sensitization Lab	ESSN Murthy
EC404PC	ICA-Linear IC Applications	D.Swathi	COUN	Counseling	T.Sadhana
EC405PC	ICA-Electronic Circuit Analysis	G.Anitha	SEM	Seminar	I.Venu
EC406PC	A&DC Lab-Analog and Digital Communications Lab	G.Narasimha/P.Krishna Rao			

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-B

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00		V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	EMF&W	ECA	LICA	LTNM	L U N C H	A&DC LAB / ICA LAB		
TUE	LTNM	ECA	A&DC	LTNM(T)/A&DC(T)		ICA	SEM	EMF&W
WED	LICA	LTNM	EMF&W	A&DC		ICA LAB / A&DC LAB		
THU	A&DC	A&DC(T)/LTNM(T)	GSLAB / ECA LAB			ECA	EMF&W	LTNM
FRI	LTNM	EMF&W	ECA	A&DC		LICA	ECA LAB / GSLAB	
SAT	EMF&W	LICA	ECA	COUN		LTNM	A&DC	ECA

(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
MA401BS	LTNM-Laplace Transforms, Numerical Methods & Complex Variables	Thirupathi Reddy	EC407PC	ICA Lab-IC Applications Lab	D.Swathi/Y.Rajani
EC402PC	EMF&W-Electromagnetic Fields and Waves	G.Narasimha	EC408PC	ECA Lab-Electronic Circuit Analysis Lab	G.Anitha/ Dr.N.Tamilarasan
EC403PC	A&DC -Analog and Digital Communications	T.Naresh	*MC409	GSLab-Gender Sensitization Lab	ESSN Murthy
EC404PC	LICA-Linear IC Applications	D.Swathi	COUN	Counseling	K.Bhaskar Reddy
EC405PC	ECA-Electronic Circuit Analysis	G.Anitha	SEM	Seminar	I.Venu
EC406PC	A&DC Lab-Analog and Digital Communications Lab	T.Naresh/S.Naresh			

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-C

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00		V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	EMF&W	A&DC	LTNM	LICA	L U N C H	SEM	ECA	LTNM
TUE	LICA	LICA	ECA LAB / GS LAB			LICA	A&DC	LTNM
WED	LTNM	EMF&W	LTNM(T)/A&DC(T)	LICA		LTNM	ECA	COUN
THU	A&DC	LICA	EMF&W	A&DC(T)/LTNM(T)		ICA LAB / A&DC LAB		
FRI	LICA	A&DC LAB / ICA LAB				ECA	EMF&W	A&DC
SAT	LICA	LTNM	A&DC	EMF&W		ECA	GS LAB / ECA LAB	

* (T) Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
MA401HS	LTNM-Laplace Transforms, Numerical Methods & Complex Variables	V.Sujatha	EC407PC	ICA Lab-IC Applications Lab	Y.Rajani/T.Sadhana
EC402PC	EMF&W-Electromagnetic Fields and Waves	Dr.S.Suresh	EC408PC	ECA Lab-Electronic Circuit Analysis Lab	Dr.S.Suresh/ Dr.N.Tamilarasan
EC403PC	A&DC-Analog and Digital Communications	S.Naresh	*MC409	GS Lab-Gender Sensitization Lab	ESSN Murthy
EC404PC	LICA-Linear IC Applications	Y.Rajani	COUN	Counseling	T.Bhavani
EC405PC	LICA-Electronic Circuit Analysis	Dr.N.Tamilarasan	SEM	Seminar	T.Bhavani
EC406PC	A&DC Lab-Analog and Digital Communications Lab	S.Naresh/T.Bhavani			

Class In Charge

HOD/ECE

Sri Indu Institute of Engineering & Technology
Sriengudal, Ibrahimpatnam,
Dist Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: III ECE-A

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	DSP	BST	VLSID	ESD	L U N C H	COUN	A&P	AI
TUE	ESD	DSP LAB / ECAD LAB				AI	DSP	A&P
WED	A&P	BST	VLSID	A&P(T)/DSP(T)		VLSID	AI	BST
THU	AI	VLSID(T) A&P(T)	DSP	DSP		ESD	SL LAB	
FRI	VLSID	AI	SEM	BST		DSP(T)/VLSID(T)	ESD	DSP
SAT	BST	A&P	ESD	VLSID		ECAD LAB / DSP LAB		

*(T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC601PC	A&P-Antennas and Propagation	P.Krishna Rao	EC604PC	DSP Lab- Digital Signal Processing Lab	K.Mallanah/T.Bhavani
EC602PC	DSP- Digital Signal Processing	G.Chandrasekhar	EC605PC	e-CAD Lab-e – CAD Lab	G.Anusha/M.Srilatha
EC603PC	VLSID- VLSI Design	M.Ganesh	EC606PC	SL Lab-Scripting Languages Lab	B.Rajeshwari/N.Mounika
EC613PE	ESD-Embedded System Design(Professional Elective - II)	K.Bhaskar Reddy	COUN	Counseling	G.Anusha
EE600OE	BST-Basics of Sensors Technology (Open Elective - I)	G.Anusha	SEM	Seminar	D.Swathi
-	AI-Artificial Intelligence	A.Sindhuja			

Class In Charge

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Sri Indu Institute of Engineering
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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: III ECE-B

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00		V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	A&P	VLSID	DSP	BST	L U N C H	DSP LAB / ECAD LAB		
TUE	BST	DSP	AI	VI SID		A&P	ESD	AI
WED	VLSID	A&P(T)/DSP(T)	ESD	BST		DSP	SL LAB	
THU	ESD	AI	VLSID(T)/A&P(T)	A&P		BST	DSP	SEM
FRI	BST	ECAD LAB / DSP LAB				ESD	A&P	VLSID
SAT	VLSID	COUN	DSP	BSI		ESD	DSP(T)/VLSID(T)	AI

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC601PC	A&P-Antennas and Propagation	P.Krishna Rao	EC604PC	DSP Lab- Digital Signal Processing Lab	G.Chandrasekhar/Y.Raju
EC602PC	DSP- Digital Signal Processing	G.Chandrasekhar	EC605PC	e-CAD Lab-e - CAD Lab	M.Ganesh/M.Srilatha
EC603PC	VLSID- VLSI Design	M.Ganesh	EC606PC	SL Lab-Scripting Languages Lab	B.Rajeshwari/N.Mounika
EC613PE	ESD-Embedded System Design(Professional Elective - II)	K.Bhaskar Reddy	COUN	Counseling	Y.Raju
EI600OE	BST-Basics of Sensors Technology (Open Elective - I)	G.Anusha	SEM	Seminar	A.Apsara
-	AI-Artificial Intelligence	A.Sindhuja			

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Sri Indu Institute of Engineering & Technology
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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: III ECE-C

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	ESD	DSP	A&P	VLSID(T)/A&P(T)	L U N C H	BST	VLSID	COUN
TUE	DSP	ESD	DSP(T)/VLSID(T)	VLSID		AI	A&P	BST
WED	BST	DSP LAB / ECAD LAB				A&P(T)/DSP(T)	BST	DSP
THU	A&P	AI	VLSID	BST		ECAD LAB / DSP LAB		
FRI	VLSID	AI	ESD	SEM		DSP	AI	A&P
SAT	ESD	A&P	SL LAB			ESD	VLSID	DSP

* (T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC601PC	A&P-Antennas and Propagation	M.Srilatha	EC604PC	DSP Lab- Digital Signal Processing Lab	Y.Raju/K.Malliah
EC602PC	DSP- Digital Signal Processing	Y.Raju	EC605PC	e-CAD Lab-e - CAD Lab	A.Sindhuja/Dr.D.Lakshmaiah
EC603PC	VLSID- VLSI Design	Dr.D.Lakshmaiah	EC606PC	SL Lab-Scripting Languages Lab	B.Rajeshwari/N.Mounika
EC613PE	ESD-Embedded System Design(Professional Elective - II)	K.Padma	COUN	Counseling	K.Padma
EE600OE	BST-Basics of Sensors Technology (Open Elective - I)	M.Tejaswini	SEM	Seminar	P.Krishna Rao
-	AI-Artificial Intelligence	K.Malliah			

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Class Time Table

CLASS: IV ECE-A

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00	
MON	S&T	S&T	GPS	OC	L U N C H	GPS	S&T	OC	
TUE	S&T	GPS	COUN	GPS		OC	MAJOR PROJECT		
WED	MAJOR PROJECT					MAJOR PROJECT			
THU	MAJOR PROJECT					MAJOR PROJECT			
FRI	MAJOR PROJECT					MAJOR PROJECT			
SAT	MAJOR PROJECT					MAJOR PROJECT			

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EE831OE	S&T-Sensors and Transducers(Open Elective - III)	I.Venu	EC801PC	Major Project	K.Padma/M.Tejaswini
EC853PE	OC-Optical Communications (Professional Elective- V)	T.Bhavani	COUN	Counseling	Y.Rajani
EC863PE	GPS-Global Positioning System(Professional Elective - VI)	K.Srikanth			

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: IV ECE-B

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	GPS	OC	OC	S&T	L U N C H	OC	GPS	S&T
TUE	GPS	OC	OC	COUN		S&T	MAJOR PROJECT	
WED	MAJOR PROJECT					MAJOR PROJECT		
THU	MAJOR PROJECT					MAJOR PROJECT		
FRI	MAJOR PROJECT					MAJOR PROJECT		
SAT	MAJOR PROJECT					MAJOR PROJECT		

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
E1831(O)	S&T-Sensors and Transducers(Open Elective – III)	I.Venu	EC801PC	Major Project	K.Srikanth/B.Jyothirmai
EC853PE	OC-Optical Communications (Professional Elective–V)	B.Jyothirmai	COUN	Counseling	K.Bhaskar Reddy
EC863PE	GPS-Global Positioning System(Professional Elective – VI)	K.Srikanth			


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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: IV ECE-C

A.Y: 2020-21

SEMESTER: II

W.E.F: 22.03.2021

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00		V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	S&T	GPS	OC	GPS	L U N C H	COUN	S&T	OC
TUE	S&T	OC	GPS	OC		S&T	MAJOR PROJECT	
WED	MAJOR PROJECT					MAJOR PROJECT		
THU	MAJOR PROJECT					MAJOR PROJECT		
FRI	MAJOR PROJECT					MAJOR PROJECT		
SAT	MAJOR PROJECT					MAJOR PROJECT		

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC8310I	S&T-Sensors and Transducers(Open Elective - III)	I.Sadhana	EC801PC	Major Project	P.Sumana/A.Apsara
EC853PI	OC-Optical Communications (Professional Elective-V)	A.Apsara	COUN	Counseling	A.Sindhuja
EC863PI	GPS-Global Positioning System(Professional Elective - VI)	P.Sumana			

P. May
Class In-Charge

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R R Dist. Telangana -501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-A

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	DSD	EDC	NATL	PTSP	L U N C H	SS	DSD LAB / EDC LAB	
TUE	EDC	PTSP	DSD	SS		COI	NATL	SS(T)/EDC(T)
WED	PTSP	DSD	SS	DSD(T)/SS(T)		NATL	BS LAB / DSD LAB	
THU	NATL	EDC(T)/DSD(T)	SS	PTSP		COI	EDC	COUN
FRI	SS	PTSP	NATL	COI		DSD	EDC	NATL
SAT	SS	EDC	DSD	PTSP		SEM	EDC LAB / BS LAB	

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC301PC	EDC-Electronic Devices and Circuits	G.Anitha	EC306PC	EDC LAB-Electronic Devices and Circuits Lab	G.Anitha/A.Sindhuja
EC302PC	NATL-Network Analysis and Transmission Lines	P.Pravallika	EC307PC	DSD LAB-Digital System Design Lab	D.Swathi/K.Hemalatha
EC303PC	DSD-Digital System Design	D.Swathi	EC308ES	BS LAB-Basic Simulation Lab	G.Chandrasekhar/P.Krishna Rao
EC304PC	SS-Signals and Systems	G.Chandrasekhar	*MC309	COI-Constitution of India	D.Srevani
EC305ES	PTSP-Probability Theory and Stochastic Processes	P.Krishna Rao	SEM	Seminar	D.Swathi
			COUN	Counseling	Dr.S.Suresh

Class In Charge

HOD/ECE

Principal
Sri Indu Institute of Engineering
& Technology
-Vijayapada(V), Ibrahimpatnam,
-Telangana -501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-B

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	SS	PTSP	DSD(T)/SS(T)	DSD	L U N C H	NATL	COI	SEM
TUE	DSD	SS	NATL	PTSP		EDC	DSD LAB / EDC LAB	
WED	SS	NATL	EDC	NATL		EDC(T)/DSD(T)	PTSP	COUN
THU	PTSP	NATL	DSD	EDC		SS	BS LAB / DSD LAB	
FRI	EDC	PTSP	SS(T)/EDC(T)	NATL		SS	EDC LAB / BS LAB	
SAT	COI	PTSP	SS	COI		EDC	DSD	NATL

*(T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC301PC	EDC-Electronic Devices and Circuits	G.Anitha	EC306PC	EDC LAB-Electronic Devices and Circuits Lab	P.Sumana/A.Sindhuja
EC302PC	NATL-Network Analysis and Transmission Lines	P.Pravallika	EC307PC	DSD LAB-Digital System Design Lab	D.Swathi/K.Hemalatha
EC303PC	DSD-Digital System Design	D.Swathi	EC308ES	BS LAB-Basic Simulation Lab	I.Venu/P.Krishna Rao
EC304PC	SS-Signals and Systems	G.Chandrasekhar	*MC309	COI-Constitution of India	R.Ramesh
EC305ES	PTSP-Probability Theory and Stochastic Processes	T.Naresh	SEM	Seminar	Y.Rajani
			COUN	Counseling	T.Naresh

Class In-Charge

HOD/ECE

SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
 Hyderabad - 501 510
 Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: II ECE-C

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	EDC	SS	DSD LAB / EDC LAB		L U N C H	DSD(T)/SS(T)	DSD	NATL
TUE	NATL	SS(T)/EDC(T)	EDC	DSD		SS	SEM	PTSP
WED	PTSP	EDC	BS LAB / DSD LAB			NATL	COI	DSD
THU	DSD	EDC(T)/DSD(T)	PTSP	NATL		SS	COUN	EDC
FRI	SS	EDC	DSD	PTSP		NATL	PTSP	COI
SAT	NATL	DSD	EDC LAB / BS LAB			COI	SS	PTSP

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC301PC	EDC-Electronic Devices and Circuits	P.Sumana	EC306PC	EDC LAB-Electronic Devices and Circuits Lab	P.Sumana/G.Anitha
EC302PC	NATL-Network Analysis and Transmission Lines	Srisailam	EC307PC	DSD LAB-Digital System Design Lab	Dr.N.Tamilarasan/ K.Hemalatha
EC303PC	DSD-Digital System Design	Dr.N.Tamilarasan	EC308ES	BS LAB-Basic Simulation Lab	I.Venu/T.Naresh
EC304PC	SS-Signals and Systems	I.Venu	*MC309	COI-Constitution of India	K.Balakrishna
EC305ES	PTSP-Probability Theory and Stochastic Processes	T.Naresh	SEM	Seminar	B.Jyothirmai
			COUN	Counseling	Dr.N.Tamilarasan

[Signature]
Class In-Charge

[Signature]
HOD/ECE

Sri Indu Institute of Engineering & Technology
Shergarh, Hyderabad
R. Dist. Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Class Time Table

CLASS: III ECE-A

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	BEFA	DCN LAB / MPMC LAB			L U N C H	CS	ACS LAB	
TUE	CS	MPMC	CO&OS	DCN		MPMC LAB / DCN LAB		
WED	CO&OS	MPMC	DCN	SEM		BEFA	CYB	DCN
THU	MPMC	CO&OS	BEFA	CYB		DCN	MPMC(T)/DCN(T)	CS
FRI	CS	BEFA	DCN(T)/CS(T)	MPMC		IPR	CS	IPR
SAT	DCN	CS(T)/MPMC(T)	IPR	CO&OS		COUN	MPMC	CYB

*(T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC501PC	MPMC-Microprocessors & Microcontrollers	Dr.S.Suresh	EC505PC	MPMC LAB-Microprocessors & Microcontrollers Lab	Dr.S.Suresh/K.Srikanth
EC502PC	DCN-Data Communications and Networks	K.Bhaskar Reddy	EC506PC	DCN LAB-Data Communications and Networks Lab	K.Bhaskar Reddy
EC503PC	CS-Control Systems	K.Srikanth	EN508HS	ACS.LAB-Advanced Communication Skills Lab	ESSN Murthy
SM504MS	BEFA-Business Economics & Financial Analysis	Sidhu	*MC510	IPR-Intellectual Property Rights	K.Lakshmi Shilpa
EC511PE	CO&OS-Computer Organization & Operating Systems (Professional Elective-I)	K.Hemalatha	SEM	Seminar	M.Tejaswini
-	CYB-Cyber Security	B.Jyothirmai	COUN	Counseling	M.Tejaswini

Class In Charge

HOD/ECE

Sri Indu Institute of Engineering
Shanguboli, Ibrahimpatnam
ECR-Dist. Telangana - 501 611



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: III ECE-B

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30 -11:20	III 11:20-12:10	IV 12:10-1:00		V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	MPMC	CO&OS	BEFA	COUN	L U N C H	IPR	DCN	CS
TUE	DCCN	CS(T)/MPMC(T)	MPMC	CYB		CO&OS	BEFA	SEM
WED	IPR	DCN LAB / MPMC LAB				CS	ACS LAB	
THU	BEFA	MPMC	DCN(T)/CS(T)	CS		MPMC LAB / DCN LAB		
FRI	DCN	CYB	BEFA	MPMC(T)/DCN(T)		CO&OS	MPMC	CO&OS
SAT	CS	MPMC	CYB	BEFA		IPR	CO&OS	DCN

(T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC501PC	MPMC-Microprocessors & Microcontrollers	Y.Rajani	EC 505PC	MPMC LAB-Microprocessors & Microcontrollers Lab	Y.Rajani/B.Jyothirmai
EC502PC	DCN-Data Communications and Networks	K.Bhaskar Reddy	EC 506PC	DCN LAB-Data Communications and Networks Lab	K.Mallalaiah
EC503PC	CS-Control Systems	K.Srikanth	EN508HS	ACS LAB-Advanced Communication Skills Lab	ESSN Murthy
SMS04MS	BEFA-Business Economics & Financial Analysis	Bharadwaj	*MC510	IPR-Intellectual Property Rights	K.Lakshmi Shilpa
EC511PE	CO&OS-Computer Organization & Operating Systems (Professional Elective-I)	K.Hemalatha	SEM	Seminar	S.Naresh
-	CYB-Cyber Security	M.Tejaswini	COUN	Counseling	K.Mallalaiah

[Signature]
Class In Charge

[Signature]
NOD/EC

[Signature]
Principal
Indu Institute of Engineering & Technology
Squad (V), Ibrahimpatnam,
Dist. Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: III ECE-C

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	DCN	IPR	CO&OS	CS	T. U. N. C. H.	CYB	MPMC	BEFA
TUE	BEFA	CS	COUN	CYB		DCN	MPMC(T)/DCN(T)	MPMC
WED	CS	CO&OS	BEFA	CYB		SEM	DCN	DCN(T)/CS(T)
THU	CO&OS	DCN	CS(T)/MPMC(T)	MPMC		IPR	CS	BEFA
FRI	MPMC	IPR	CO&OS	CS		MPMC LAB / DCN LAB		
SAT	BEFA	DCN LAB / MPMC LAB				MPMC	ACS LAB	

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC501PA	MPMC-Microprocessors & Microcontrollers	Y.Rajam	EC505PC	MPMC LAB-Microprocessors & Microcontrollers Lab	B.Jyothirmal/I.Sadhana
EC502PC	DCN-Data Communications and Networks	K.Malliah	EC506PC	DCN LAB-Data Communications and Networks Lab	K.Malliah
EC503PC	CS-Control Systems	I.Sadhana	EN508HS	ACS LAB-Advanced Communication Skills Lab	A.Mahesh Babu
SM504MS	BEFA-Business Economics & Financial Analysis	Hharadwaj	*MC510	IPR-Intellectual Property Rights	K.Lakshmi Shilpa
EC511PI	CO&OS-Computer Organization & Operating Systems (Professional Elective-I)	M.Tejaswini	SEM	Seminar	M.Tejaswini
-	CYB-Cyber Security	T.Bhavani	COUN	Counseling	T.Sadhana

Class In Charge

HOD/ECE

Principal for e-learning
Dr. Telangana-501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: IV ECE-A

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	CN	EMI	WCN	MWE	L U N C H	VLSI & E-CAD Lab / MWE LAB		
TUE	MWE	VLSID	CN	EMI		COUN	SEMINAR	
WED	WCN	EMI	CN	VLSID		MWE(T)/VLSID(T)	MWE	VLSID
THU	EMI	CN	WCN	VLSID		MWE LAB / VLSI & E-CAD Lab		
FRI	CN	MWE	VLSID(T)/MWE(T)	EMI		WCN	WCN	VLSID
SAT	CN	IOMP				VLSID	WCN	MWE

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC701PC	MWE - Microwave Engineering	G.Narasimha	EC703PC	VLSI & E-CAD Lab-VLSI and E-CAD Lab	M.Ganesh/M.Tejaswini
EC721PE	CN-Computer Networks (Professional Elective - II)	K.Padma	EC704PC	MWE Lab-Microwave Engineering Lab	G.Narasimha/S.Naresh
EC731PE	WCN-Wireless Communications and Networks (Professional Elective - III)	S.Naresh	EC705PC	IOMP-Industry Oriented Mini Project	K.Bhaskar Reddy
EC743PE	EMI-Electronic Measurements and Instrumentation (Professional Elective - IV)	A.Sindhuja	EC706PC	Seminar	T.Naresh
EC702PC	VLSID-VLSI Design	M.Ganesh	COUN	Counseling	A.Sindhuja

Class In Charge

HOD/ECE

Principal
Sri Indu Institute of Engineering & Technology
Trigunapada, Ibrahimpatnam,
West. Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: IV ECE-B

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

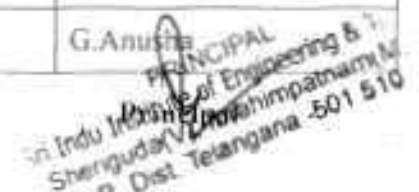
	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	VLSID	MWE	MWE(T)/VLSID(T)	CN	L U N C H	EMI	SEMINAR	
TUE	CN	WCN	VLSID	MWE		VLSI & E-CAD Lab / MWE LAB		
WED	EMI	VLSID(T)/MWE(T)	MWE	COUN		WCN	WCN	EMI
THU	WCN	MWE	VLSID	CN		EMI	EMI	CN
FRI	WCN	EMI	CN	VLSID		MWE LAB / VLSI & E-CAD Lab		
SAT	MWE		IOMP			EMI	VLSID	WCN

*(T) – Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC701PC	MWE-Microwave Engineering	G.Narasimha	EC703PC	VLSI & E-CAD Lab-VLSI and E-CAD Lab	M.Tejaswir/K.Padma
EC721PE	CN-Computer Networks (Professional Elective – II)	K.Padma	EC704PC	MWE Lab-Microwave Engineering Lab	T.Bhavani/G.Anusha
EC731PE	WCN-Wireless Communications and Networks (Professional Elective – III)	S.Naresh	EC705PC	IOMP-Industry Oriented Mini Project	G.Narasimha
EC743PE	EMI-Electronic Measurements and Instrumentation (Professional Elective – IV)	A.Sindhuja	EC706PC	Seminar	G.Chandrasekhar
EC702PC	VLSID-VLSI Design	M.Ganesh	COUN	Counseling	G.Anusha


Class In-Charge


HOD/ECE


PRINCIPAL
Sri Indu Institute of Engineering & Technology
Sherguda, Warangal, Telangana - 501 510



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Class Time Table

CLASS: IV ECE-C

A.Y: 2020-21

SEMESTER: I

W.E.F: 01.09.2020

	I 9:40-10:30	II 10:30-11:20	III 11:20-12:10	IV 12:10-1:00	1:00-1:30	V 1:30-2:20	VI 2:20-3:10	VII 3:10-4:00
MON	MWE	EMI	WCN	VLSID	L U N C H	EMI	CN	MWE(T)/VLSID(T)
TUE	VLSID	MWE	EMI	WCN		CN	CN	WCN
WED	EMI	MWE	VLSID	CN		VLSI & E-CAD Lab / MWE LAB		
THU	CN	WCN	VLSID(T)/MWE(T)	EMI		MWE	SEMINAR	
FRI	CN	EMI	MWE	WCN		COUN	VLSID	VLSID
SAT	WCN	IOMP				MWE LAB / VLSI & E-CAD Lab		

*(T) - Tutorial Concern Faculty

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
EC701PC	MWE - Microwave Engineering	T.Bhavani	EC703PC	VLSI & E-CAD Lab-VLSI and E-CAD Lab	K.Padma/ Dr.D.Lakshmaiah
EC721PE	CN-Computer Networks (Professional Elective - II)	T.Sadhana	EC704PC	MWE Lab-Microwave Engineering Lab	T.Bhavani/G.Anusha
EC751PE	WCN-Wireless Communications and Networks (Professional Elective - III)	B.Jyothirmai	EC705PC	IOMP-Industry Oriented Mini Project	G.Anusha
EC743PE	EMI-Electronic Measurements and Instrumentation (Professional Elective - IV)	G.Anusha	EC706PC	Seminar	T.Naresh
EC702PC	VLSID-VLSI Design	Dr.D.Lakshmaiah	COUN	Counseling	K.Srinanth

G. Anusha
Class In-Charge

HOD, ECE

K. Srinanth
PRINCIPAL
Sri Indu Institute of Engineering & Technology
Sherguda (V), Ibrahimpatnam (M)
Dist. Telangana - 501 516



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Faculty Workload

A.Y : 2020-21

SEMESTER: I

S.No	Name Of The Faculty	Subject / Lab Name	Tutorials	Others as per Time table	Total No. of Hrs.
1	Dr.D.Lakshmaiah	1.VLSID IV ECE-C(5) 2.VLSID&E-CAD Lab IV ECE-C(6)	1.VLSID IV ECE-C(2)	-	13
2	Dr.S.Suresh	1.MPMC III ECE-A(5) 2.MPMC LAB-III ECE-A(6)	1.MPMC III ECE-A(2)	1.Counseling II ECE-A(1)	14
3	Dr.N.Tamilarasan	1.DSD II ECE-C(6) 2.DSD LAB II ECE-C(4)	1.DSD II ECE-C(2)	1.Counseling II ECE-C(1)	13
4	G.Anitha	1.EDC II ECE-A(5) 2.EDC LAB II ECE-A(4) 3.EDC II ECE-B(4) 4.EDC LAB II ECE-C(4)	1.EDC II ECE-A(2) 2.EDC II ECE-B(2)	-	21
5	D.Swathi	1.DSD II ECE-A(5) 2.DSD LAB II ECE-A(4) 3.DSD II ECE-B(4) 4.DSD LAB II ECE-B(4)	1.DSD II ECE-A(2) 2.DSD II ECE-B(2)	1.Seminar II ECE-A(1)	22
6	G.Chandrasekhar	1.SS II ECE-A(6) 2.BS LAB II ECE-A(4) 3.SS II ECE-B(6)	1.SS II ECE-A(2) 2.SS II ECE-B(2)	1.Seminar IV ECE-B(2)	22
7	P.Krishna Rao	1.PTSP II ECE-A(6) 2.BS LAB II ECE-A(4) 3.BS LAB II ECE-B(4)	-	-	14



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Faculty Workload

8	A.Sindhuja	1.EDC LAB II ECE-A(4) 2.EDC LAB II ECE-B(4) 3.EMI IV ECE-A(5) 4.EMI IV ECE-B(6)	-	1.Counseling IV ECE-A(1)	20
9	K.Hemalatha	1.DSD LAB II ECE-A(4) 2.DSD LAB II ECE-B(4) 3.CO&OS III ECE-A(4) 4.CO&OS III ECE-B(4)	-	-	16
10	T.Naresh	1.PTSP II ECE-B(5) 2.PTSP II ECE-C(6) 3.BS LAB II ECE-C(4)	-	1.Counseling II ECE-B(1) 2.Seminar IV ECE-A(2) 3.Seminar IV ECE-C(2)	20
11	P.Sumana	1.EDC LAB II ECE-B(4) 2.EDC II ECE-C(5) 3.EDC LAB II ECE-C(4)	1.EDC II ECE-C(2)	-	15
12	L.Venu	1.BS LAB II ECE-B(4) 2.SS II ECE-C(5) 3.BS LAB II ECE-C(4)	1.SS II ECE-C(2)	-	15
13	Y.Rajani	1.MPMC III ECE-B(5) 2.MPMC LAB III ECE-B(6) 3.MPMC III ECE-C(5)	1.MPMC III ECE-B(2) 2.MPMC III ECE-C(2)	1.Seminar II ECE-B(1)	21
14	B.Jyothirmai	1.CYB III ECE-A(3) 2.MPMC LAB III ECE-B(6) 3.MPMC LAB III ECE-C(6) 4.WCN IV ECE-C(6)	-	1.Seminar II ECE-C(1)	22
15	K.Bhaskar Reddy	1.DCCN III ECE-A(5) 2.DCCN LAB III ECE-A(6) 3.DCCN III ECE-B(4)	1.DCCN III ECE-A(2) 2.DCCN III ECE-B(2)	1.IOMP IV ECE-A(3)	22



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Faculty Workload

16	K.Srikanth	1.CS III ECE-A(5) 2.MPMC LAB III ECE-A(6) 3.CS III ECE-B(4)	1.CS III ECE-A(2) 2.CS III ECE-B(2)	1.Counseling IV ECE-C(1)	20
17	M.Tejaswini	1.CYB III ECE-B(3) 2.CO&OS III ECE-C(4) 3.VLSI&E-CAD Lab IV ECE-A(6) 4.VLSI&E-CAD Lab IV ECE-B(6)	-	1.Seminar III ECE-A(1) 2.Counseling III ECE-A(1) 3.Seminar III ECE-C(1)	22
18	K.Malliah	1.DCN LAB III ECE-B(6) 2.DCN III ECE-C(4) 3.DCN LAB III ECE-C(6)	1.DCN III ECE-C(2)	1.Counseling III ECE-B(1)	19
19	S.Naresh	1.WCN IV ECE-A(6) 2.MWE LAB IV ECE-A(6) 3.WCN IV ECE-C(6)	-	1.Seminar III ECE-B(1)	19
20	T.Sadhana	1.CS III ECE-C(5) 2.MPMC LAB III ECE-C(6) 3.CN IV ECE-C(6)	1.CS III ECE-C(2)	1.Counseling III ECE-C(1)	20
21	T.Bhavani	1.CYB III ECE-C(3) 2.MWE LAB IV ECE-B(6) 3.MWE IV ECE-C(5) 4.MWE LAB IV ECE-C(6)	1.MWE IV ECE-C(2)	-	22
22	G.Narasimha	1.MWE IV ECE-A(5) 2.MWE LAB IV ECE-A(6) 3.MWE IV ECE-B(5)	1.MWE IV ECE-A(2) 2.MWE IV ECE-B(2)	1.IOMP IV ECE-B(3)	23
23	K.Padma	1.CN IV ECE-A(6) 2.CN IV ECE-B(5) 3.VLSI&E-CAD Lab IV ECE-B(6) 4.VLSI&E-CAD Lab IV ECE-C(6)	-	-	23



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Faculty Workload

24	M.Ganesh	1.VLSID IV ECE-A(5) 2.VLSID&E-CAD Lab IV ECE-A(6) 3. VLSID IV ECE-B(5)	1.VLSID IV ECE-A(2) 2.VLSID IV ECE-B(2)	=	20
25	G.Anusha	1.MWF LAB IV ECE-B(6) 2.EMI IV ECE-C(6) 3.MWF LAB IV ECE-C(6)	-	1.Counseling IV ECE-B(1) 2.IOMP IV ECE-C(3)	22

[Signature]
Head of the Department
Electronics and Communication Engrs. Dept.
SRI INDU INSTITUTE OF ENGG & TECH.
Sherguda(V), Bheemipatnam, R.R. Dist. Srisilapeta-500 050

[Signature]
PRINCIPAL
Sri Indu Institute of Engineering & Tech.
Sherguda(V), Bheemipatnam,
R.R. Dist. Srisilapeta-500 050



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Faculty Workload

A.Y : 2020-21

SEMESTER: II

S.No	Name Of The Faculty	Subject / Lab Name	Tutorials	Others as per Time table	Total No. of Hrs.
1	Dr.D.Lakshminah	1.VLSI III ECE-C(5) 2.e-CAD Lab III ECE-C(6)	1.VLSI III ECE-C(2)	-	13
2	Dr.S.Suresh	1.ECA Lab II ECE-A(4) 2.FM&W II ECE-C(5) 3.ECA Lab II ECE-C(4)	-	-	13
3	Dr.N.Tamilisavan	1.ECA Lab II ECE-B(4) 2.ECA II ECE-C(6) 3.ECA Lab II ECE-C(4)	-	-	14
4	G.Narasimhan	1.FM&W II ECE-A(6) 2.A&DC Lab II ECE-A(6) 3.FM&W II ECE-B(6)	-	-	18
5	I.Narasimhan	1.A&DC II ECE-A(5) 2.A&DC II ECE-B(5) 3.A&DC Lab II ECE-B(6)	1.A&DC II ECE-A(2) 2.A&DC II ECE-B(2)	-	20
6	D.Swathi	1.LICA II ECE-A(5) 2.LICA II ECE-B(5) 3.LICA Lab II ECE-B(6)	-	1.Seminar III ECE-A(1)	17
7	G.Anitha	1.ECA II ECE-A(6) 2.ECA II ECE-B(6) 3.ECA Lab II ECE-B(4)	-	-	16



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8	P.Krishna Rao	1.A&DC Lab II ECE-A(6) 2.A&P III ECE-A(4) 3.A&P III ECE-B(4)	1. A&P III ECE-A(4) 2. A&P III ECE-B(4)	1.Seminar III ECE-C(1)	23
9	J.Sudhana	1.ICA Lab II ECE-A(6) 2.ICA Lab II ECE-C(6) 3.S&I IV ECE-C(4)	-	1.Counselling II ECE-A(1) 2. Project IV ECE-B(3)	20
10	J.Venu	1.ICA Lab II ECE-A(6) 2.S&I IV ECE-A(4) 3.S&I IV ECE-B(4)	-	1.Seminar II ECE-A(1) 2.Seminar II ECE-B(1)	16
11	K.Bhaskar Reddy	1.ICA Lab II ECE-A(4) 2.FSD III ECE-A(5) 3.FSD III ECE-B(5)	-	1.Counselling II ECE-B(1) 2.Counseling IV ECE-B(1)	16
12	S.Naresh	1.A&DC Lab II ECE-B(6) 2.A&DC II ECE-C(5) 3.A&DC Lab II ECE-C(6)	1.A&DC II ECE-C(2)	-	19
13	Y.Rajani	1.ICA Lab II ECE-B(6) 2.ICA II ECE-C(5) 3.ICA Lab II ECE-C(6)	-	1.Counseling IV ECE-A(1)	18
14	T.Bhavani	1.A&DC Lab II ECE-C(6) 2.DSP Lab III ECE-A(6) 3.OC IV ECE-A(4)	-	1.Counseling II ECE-C(1) 2.Seminar II ECE-C(1)	18
15	G.Chandrasekhar	1.DSP III ECE-A(6) 2.DSP III ECE-B(5) 3.DSP Lab III ECE-B(6)	1.DSP III ECE-A(2) 2.DSP III ECE-B(2)	-	21



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16	M.Ganesh	1.VLSID III ECE-A(5) 2.VLSID III ECE-B(5) 3.e-CAD Lab III ECE-B(6)	1 VLSID III ECE-A(2) 2.VLSID III ECE-B(2)	.	20
17	M.Tejaswini	1.BST III ECE-C(5)	.	1. Major Project IV ECE-A(12)	17
18	A.Sindhuja	1.AI III ECE-A(4) 2.AI III ECE-B(4) 3.e-CAD Lab III ECE-C(6)	.	1.Counseling IV ECE-C(1)	15
19	K.Malliah	1.DSP Lab III ECE-A(6) 2.AI III ECE-C(4) 3.DSP Lab III ECE-C(6)	.	.	16
20	M.Srilatha	1.e-CAD Lab III ECE-A(6) 2.e-CAD Lab III ECE-B(6) 3.A&P III ECE-C(5)	1.A&P III ECE-C(2)	.	19
21	B.Jyothirmai	1.OC IV ECE-B(4)	.	1.Major Project IV ECE-B(12)	16
22	A.Apsara	1.OC IV ECE-C(4)	.	1.Seminar III ECE-B(1) 2.Major Project IV ECE-C(13)	18
23	K.Padma	1.I SD III ECE-C(5)	.	1.Counseling III ECE-C(1) 2.Major Project IV ECE-A(12)	18



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24	K.Srikanth	1. GPS IV ECE-A(4) 2. GPS IV ECE-B(4)	-	Major Project IV ECE-B(12)	20
25	P.Sumana	1. GPS IV ECE-C(4)		Major Project IV ECE-C(12)	16
26	G.Anusha	1. BST III ECE-A(5) 2. e-CAD Lab III ECE-A(6) 3. BST III ECE-B(5)	-	Counselling III ECE-A(1)	18
27	Y.Raju	1. DSP Lab III ECE-B(6) 2. DSP III ECE-C(5) 3. DSP Lab III ECE-C(6)	1. DSP III ECE-C(2)	Counselling III ECE-B(1)	20

Caro
Head of the Department
Electronics and Communication Engg. Dept.
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Course/Subject Name
Prepared by
Dept
AY
Class
Institute V/M
Department V / M /PEO
POs /PSOs
Course Syllabus with Structure
Course Outcomes (CO)
Mapping CO with PO/PSO; Course with PO/PSO with Justification
Academic Calendar
Time table - highlighting your course periods including tutorial
Lesson plan with number of hours/periods, TA/TM, Text/Reference book
Gap within the syllabus - mapping to CO, PO/PSO
Gaps beyond the syllabus - Mapping to PO/PSO
Gaps addressed by a resource person - document
Gaps addressed by any other teaching aid/methodology
Web references
Lecture notes
List of Power point presentations / Videos
CD with PPT/Videos
University Question papers
Internal Question papers, Key with CO and BT
Assignment Question papers mapped with CO and BT
Scheme of evaluation with CO and BT mapping
Tutorial topics with evidence
Result Analysis to identify weak and advanced learners - 3 times in a semester
Result Analysis at the end of the course
Remedial class for weak students - schedule and evidences
Advance Learners- Engagement documentation
Course Assessment (Plan & Execution)
CO, PO/PSO attainment sheets
Observation for not attaining CO or for improvement
Plan of action to improve CO attainment next time
Attendance register (Theory/Tutorial/Remedial) - Teacher/Course delivery record; Continuous evaluation
Course file (Digital form)




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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
Electronics and Communication Engineering

2020-2021, 1st semester

COURSE OUTCOMES

COURSE: Digital System Design (C213)

CLASS: II- ECE A&B

Course Outcomes:

After completing this course the student will be able to:

C213.1: State the Boolean algebra, different number systems and codes. (Knowledge)

C213.2: Design the different combinational logic circuits.(Synthesis)

C213.3: Design the different Sequential circuits. (Synthesis)

C213.4: Design synchronous and asynchronous counters. (Synthesis)

C213.5: Define, Differentiate between logic families and realization of logic gates using diodes and transistors. (Knowledge, Analysis)

C213.6: Design the digital system. (Synthesis)


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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
Electronics and Communication Engineering

2020-2021, 1st semester

COs and Mapping with PO/PSO

COURSE: Digital System Design (C213)

CLASS: II- ECE A&B

Course Outcomes:

After completing this course the student will be able to:

C213.1: State the Boolean algebra, different number systems and codes. (Knowledge)

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C213.4: Design synchronous and asynchronous counters. (Synthesis)

C213.5: Define, Differentiate between logic families and realization of logic gates using diodes and transistors. (Knowledge, Analysis)

C213.6: Design the digital system. (Synthesis)

Mapping of course outcomes with program outcomes:

High -3 Medium -2 Low-1

PO / CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
C213.1	3	2	-	-	-	-	-	-	-	-	-	-
C213.2	3	3	3	-	-	-	-	-	-	-	-	-
C213.3	3	2	3	-	-	-	-	-	-	-	-	-
C213.4	3	1	3	-	-	-	-	-	-	-	-	-
C213.5	3		3	-	-	-	-	-	-	-	-	-
C213.6	3	-	3	-	-	-	-	-	-	-	-	-
C213	3	2	3	-	-	-	-	-	-	-	-	-


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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
Electronics and Communication Engineering

2020-2021, 1st semester

CO- PO/PSO Mapping - Justification

CO-PO mapping Justification

- PO1. ENGINEERING KNOWLEDGE:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- PO2. PROBLEM ANALYSIS:** Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- PO3. DESIGN/DEVELOPMENT OF SOLUTIONS:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

C213.1: State the Boolean algebra, different number systems and codes. (Knowledge)

Change one number system into another number system.(Application)

	Justification
PO1	Students get the knowledge of Boolean algebra, different number systems, codes and different logic gates. (level 3)
PO2	Students solve problems on number system, conversion of SOP&POS forms and Multilevel NAND/NOR Realizations. (level 2)

C213.2: Design the different combinational logic circuits.(Synthesis)

Modify and transform one form of Boolean equation to another form and simplify the Boolean equation in Karnaugh Map.(Application, Comprehension)

	Justification
PO1	Students get the knowledge on Combinational circuits and Karnaugh map(level 3)
PO2	Students solve problems on Karnaugh map and Tabular Method.(level 3)
PO3	Students able to design Adders,Comparators and Encoders. (level 3)




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C213.3: Design the different Sequential circuits.(Synthesis)

Analyze and compare the flipflops and transform one flipflop to another flipflop.

(Analysis, Evaluation)

	Justification
PO1	Students get the knowledge on Flipflops, Registers and Counters.(level 3)
PO2	Students solve problems on .(level 2)
PO3	Students able to design Adders,Comparators and Encoders. (level 3)

C213.4: Design synchronous and asynchronous counters.(Synthesis)

Analyze and differentiate the sequential machine.(Analysis)

	Justification
PO1	Students get the knowledge on State diagram, Parity-bit generator and FSM.(level 3)
PO2	Students solve problems on Sequence Detector.(level 1)
PO3	Students able to design Synchronous Sequential circuits, Synchronous and Asynchronous counters. (level 3)

C213.5: Define and Differentiate between logic families and realization of logic gates using

diodes and transistors.(Knowledge, Analysis)

	Justification
PO1	Students get the knowledge on logic families.(level 3)
PO3	Students able to realization of basic gates using diodes and transistors .(level 2)

C213.6: Design the digital system.(Synthesis)

	Justification
PO1	Students get the knowledge on Logic gates, Boolean Algebra,K-Map,Combinational and Sequential circuits.(level 3)
PO3	Students able to design Digital System. (level 3)


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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

LESSON PLAN

Programme: B.Tech	Academic Year: 2020-2021
Year: II	Semester: I
Course Title: Digital System Design	Course Code: EC303PC
Name of Faculty: Dheekonda Swathi	Number of lectures per week:5

UNIT - I:

Number Systems: Number systems, Complements of Numbers, Codes- Weighted and Non-weighted codes and its Properties, Parity check code and Hamming code.

Boolean Algebra: Basic Theorems and Properties, Switching Functions- Canonical and Standard Form, Algebraic Simplification, Digital Logic Gates, EX-OR gates, Universal Gates, Multilevel NAND/NOR realizations..

No. of Sessions Planned	Topics	Reference	Teaching Method/ Aids
2	Review of number systems	R1,R2	PPT
1	Complements of Numbers	R1,R2	PPT
1	Binary Codes	R1	PPT
2	Binary Coded Decimal Code and its Properties	R1	PPT
1	Unit Distance Codes	R1	PPT
1	Error Detecting and Correcting Codes	R1	PPT
2	Basic Theorems and Properties	R1,W1	PPT
1	Switching Functions	R1	PPT
1	Canonical and Standard Form	R1	PPT
1	Problems on hamming codes and minimization of switching functions.	R1,T1	PPT
1	Digital Logic Gates	R1,R2	PPT
1	Algebraic Simplification of Digital Logic Gates	R1	PPT
1	Properties of XOR Gates	R1	PPT
1	Universal Gates	R1,R2	PPT
2	Multilevel NAND/NOR realizations	R1	PPT



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UNIT - II:

Minimization of Boolean functions: Karnaugh Map Method - Up to five Variables, Don't Care Map Entries, Tabular Method,

Combinational Logic Circuits: Adders, Subtractors, Comparators, Multiplexers, Demultiplexers, Encoders, Decoders and Code converters, Hazards and Hazard Free Relations.

No. of Sessions Planned	Topics	Reference	Teaching Method/ Aids
1	Introduction, The Minimization of switching function using theorem	R1,R2	PPT
2	The Karnaugh Map Method-Up to Five Variable Maps	R1,R2,W2	PPT
1	Don't Care Map Entries	R1,W2	PPT
2	Tabular Method	R1	PPT
1	Design of Combinational Logic: Adders	R1,R2	PPT
1	Subtractors ,comparators	R1	PPT
1	Multiplexers, Demultiplexers	R1	PPT
1	Decoders, Encoders	R1	PPT
2	Code converters	R1	PPT
1	Problems on K-map, Tabular method and design of combinational circuits.	R1,T1	PPT
1	Hazards and Hazard Free Relations	R1	PPT

UNIT - III

Sequential Circuits Fundamentals: Basic Architectural Distinctions between Combinational and Sequential circuits, SR Latch, Flip Flops: SR, JK, JK Master Slave, D and T Type FlipFlops, Excitation Table of all Flip Flops, Timing and Triggering Consideration, Conversion from one type of Flip-Flop to another.

Registers and Counters: Shift Registers - Left, Right and Bidirectional Shift Registers, Applications of Shift Registers - Design and Operation of Ring and Twisted Ring Counter, Operation of Asynchronous and Synchronous Counters.

No. of Sessions Planned	Topics	Referen ce	Teaching Method/ Aids
1	Basic Architectural Distinctions between Combinational and Sequential circuits	R1	PPT
1	Latches, Flip Flops	R1	PPT
2	SR, JK, Race Around Condition in JK	R1	PPT
1	JK Master Slave	R1	PPT
1	D and T Type Flip Flops		PPT
2	Excitation Table of all Flip Flops	R1	PPT
1	Design of a Clocked Flip-Flop	R1	PPT
1	Timing and Triggering Consideration	R1	PPT
2	Conversion from one type of Flip-Flop to another.	R1	PPT
1	Shift Registers, Data Transmission in Shift Registers	R1,R2	PPT
1	Operation of Shift Registers and its applications	R1,R2	PPT



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1	Design and Operation of Ring Counter	R1	PPT
1	Applications of Shift Registers.	R1,R2	PPT
1	Twisted Ring Counter	R1	PPT
2	Operation Of Asynchronous And Synchronous Counters.	R1	PPT

UNIT - IV

Sequential Machines: Finite State Machines, Synthesis of Synchronous Sequential Circuits- Serial Binary Adder, Sequence Detector, Parity-bit Generator, Synchronous Modulo N -Counters, Finite state machine-capabilities and limitations, Mealy and Moore models.

No. of Sessions Planned	Topics	Reference	Teaching Method/ Aids
1	Introduction to finite state machines	R1	PPT
2	Synthesis of Synchronous Sequential Circuits	R1	PPT
1	Serial Binary Adder	R1	PPT
1	Sequence Detector	R1	PPT
1	Parity-bit Generator	R1	PPT
1	Design of Synchronous Modulo N – Counters	R1	PPT
1	Capabilities and limitations of Finite state machine	R1	PPT
2	Mealy and Moore models	R1	PPT

UNIT - V

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL, CML and CMOS Logic Families and its Comparison, Classification of Integrated circuits, comparison of various logic families, standard TTL NAND Gate-Analysis & characteristics, TTL open collector O/Ps, Tristate TTL, MOS & CMOS open drain and tri state outputs, CMOS transmission gate, IC interfacing- TTL driving CMOS & CMOS driving TTL.

No. of Sessions Planned	Topics	Reference	Teaching Method/ Aids
2	Realization of logic gates using Diodes and Transistors	R1,T1	PPT
2	Explain about Logic Families: DCTL, RTL, DTL,TTL,CML and CMOS	R1	PPT
1	Comparison of various logic families	R1	PPT
1	Classification of Integrated circuits	R1	PPT
2	TTL NAND Gate-Analysis & characteristics	R1	PPT
2	TTL open collector O/Ps, Tristate TTL	R1	PPT
2	MOS & CMOS open drain and tristate outputs	R1	PPT
1	CMOS transmission gate	R1,T1	PPT
2	IC interfacing- TTL driving CMOS & CMOS driving TTL	R1,T1	PPT



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TEXT BOOKS:

T1. Switching and Finite Automata Theory- Zvi Kohavi & Niraj K. Jha, 3rd Edition, Cambridge.

REFERENCE BOOKS:

R1. Switching Theory and Logic Design – A Anand Kumar, 3rd Edition, PHI, 2013

R2. Digital Design- Morris Mano, 4th Edition, Pearson.

WEB REFERENCES:

W1. <https://nptel.ac.in/courses/117105080/7>

W2. https://en.wikipedia.org/wiki/Karnaugh_map

W3. <https://nptel.ac.in/courses/117105080/32>




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