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1.1.1 The Institution ensures effective curriculum delivery through a well planned and documented process

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Sri Indu Institute of Engineering & Tech. Sheriguda(V), Ibrahimpatnam(M), R.R. Dist. Telangana.-501 510

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

Academic Calendar 2021-22

B. TECH./B.PHARM. III & IV YEARS I & II SEMESTERS

ISEM

S. No	Description	Duration	
		From	To
1	Commencement of I Semester classwork	District Co.	06.092021
2	1 st Spell of Instructions (including Dussehra Recess)	06.09.2021	06.11.2021 (9 Weeks)
3	Dussehra Recess	11.10.2021	16.10.2021 (1 Week)
4	First Mid Term Examinations	08.11.2021	13.11.2021 (1 Week)
5	Submission of First Mid Term Exam Marks to the University on or before	20.11.2021	
6	2 nd Spell of Instructions	15.11.2021	08.01.2022 (8 Weeks)
7	Second Mid Term Examinations	10.01.2022	18.01.2022 (1 Week)
8	Preparation Holidays and Practical Examinations	19.01.2022	25.01.2022 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before	25.01.2022	
10	End Semester Examinations	27.01.2022	09.02.2022

II SEM

S. No	Description	Duration	
9. 140		From	To
1	Commencement of Il Semester classwork	A RESIDE	10.02.2022
2	1st Spell of Instructions	10.02.2022	06.04.2022 (8 Weeks)
3	First Mid Term Examinations	07.04.2022	13.04.2022 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before		
5	2 nd Spell of Instructions (including Summer Vacation)	16.04.2022	24.06.2022 (10 Weeks)
6	Summer Vacation	09.05.2022	21.05.2022 (2 Weeks)
7	Second Mid Term Examinations	25.06.2022	01.07.2022 (1 Week)
8	Preparation Holidays and Practical Examinations	02.07.2022	09.07.2022 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before	09.07.2022	
16	End Semester Examinations	11.07.2022	23.07.2022 (2 Weeks)

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R.R. Dist. Telangana .501 510

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD ACADEMIC CALENDAR 2021-22

B. TECH/B.PHARM. II YEAR I & II SEMESTERS

ISEM

S. No	Description		Duration
D. 140		From	To
1	Dussehra Recess	11.10.2021	16.10.2021 (1 Week)
2	Commencement of I Semester classwork		18.10.2021
3	1st Spell of Instructions	18.10.2021	11.12.2021 (8 Weeks)
4	First Mid Term Examinations	13.12.2021	18.12.2021 (1 Week)
5	Submission of First Mid Term Exam Marks to the University on or before	24.12.2021	
6	2 nd Spell of Instructions	20.12.2021	12.02.2022 (8 Weeks)
7	Second Mid Term Examinations	14.02.2022	19.02.2022 (1 Week)
8	Preparation Holidays and Practical Examinations	21.02.2022	26.02.2022 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before	26.02.2022	
10	End Semester Examinations	28.02.2022	12.03.2022 (2 Weeks)

II SEM

e N.	Description	SH SH	Duration
S. No		From	To
1	Commencement of II Semester classwork	MAN A THE THE	14.03.2022
2	1st Spell of Instructions (including Summer Vacation)	14.03.2022	28.05.2022 (11Weeks)
3	Summer Vacation	09.05.2022	21.05.2022 (2 Weeks)
4	First Mid Term Examinations	30.05.2022	04.06.2022 (1 Week)
5	Submission of First Mid Term Exam Marks to the University on or before	11.06.2022	
6	2 nd Spell of Instructions	06.06.2022	30.07.2022 (8 Weeks)
7	Second Mid Term Examinations	01.08.2022	06.08.2022 (1 Week)
8	Preparation Holidays and Practical Examinations	09.08.2022	16.08.2022 (1 Week)
9	Submission of Second Mid Term Exam Marks to the University on or before	16,08,2022	
10	End Semester Examinations	17.08.2022	30.08.2022 (2 Weeks)

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Sheriguda(V). Tetangana .501 510

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD ACADEMIC CALENDAR 2021-22

B. TECH/B.PHARM. I YEAR I & II SEMESTERS

I SEM

S. No	Description	Duration	
		From	To
1	Induction programme	09.12.	2021 to 18.12.2021
2	1# Spell of Instructions	20.12.2021	12.02.2022 (8 Weeks)
3	First Mid Term Examinations	14.02.2022	19.02.2022 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before	The second secon	
5	2 nd Spell of Instructions	21.02.2022	23.04.2022 (9 Weeks)
6	Second Mid Term Examinations	25.04.2022	30.04.2022 (1 Week)
7	Preparation Holidays and Practical Examinations	02.05.2022	07.05.2022 (1 Week)
8	Submission of Second Mid Term Exam Marks to the University on or before	07.05.2022	
9	End Semester Examinations	09.05.2022	21.05.2022 (2 Weeks)

II SEM

a . s	Description	Duration	
S. No		From	To
1	Commencement of II Semester classwork	- C188LB	23.05.2022
2	1st Spell of Instructions	23.05.2022	16.07.2022 (8 Weeks)
3	First Mid Term Examinations	18.07.2022	23.07.2022 (1 Week)
4	Submission of First Mid Term Exam Marks to the University on or before	30.07.2022	
5	2 nd Spell of Instructions	26.07.2022	17.09.2022 (8 Weeks)
6	Second Mid Term Examinations	19.09.2022	24.09.2022 (1 Week)
7	Preparation Holidays and Practical Examinations	26.09.2022	01.10.2022 (1 Week)
8	Submission of Second Mid Term Exam Marks to the University on or before	01.10.2022	
9	End Semester Examinations	03.10.2022	18.10.2022 (2 Weeks)

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Vision

To become a premier institute of academic excellence by providing the world class education that transforms individuals into high intellectuals, by evolving them as empathetic and responsible citizens through continuous improvement.

Mission

- To offer outcome-based education and enhancement of technical and practical skills.
- To continuously assess teaching-learning process through institute-industry collaboration.
- To be a centre of excellence for innovative and emerging fields in technology development with state-of-art facilities to faculty and students' fraternity.
- To Create an enterprising environment to ensure culture, ethics and social responsibility among the stakeholders.

Quality Policy

- To ensure students to uphold moral and ethical values.
- To expose the students to understand the socio-economic strata of the society with an empathetic attitude.
- To nurture talent and entrepreneurship and enable all-round development in students.
- To cater to the demand-driven needs of various stake holders. To continually improve all the
 processes through endorsing cognizance, conducting periodical reviews and unifying trainings at
 all levels.



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Sheriguda(V), Ibrahimpatnam(M)
R.R. Dist. Telangana, 501 510

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(Formerly RVR Institute of Engineering & Technology)
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Main Road, Sheriguda, Ibrahimpatnam, R.R. Dist. 501 510.
Campus Ph: 9640590999, 9347187999, 8096951507.

Institution academic calendar- 2021-22

B. Tech III, IV Years - I Semester

S.No	Event	Date	Duration
1	Commencement of I semester class work	06-09-2021	***************************************
2	Commencement of class work	06-09-2021 To 06-11-2021	9 weeks
3	Traditional Day	09-10-2021	1Day
4	Dussehra Vacation	11-10-2021 To 16-10-2021	1 week
5	1st Mid Term Examinations	08-11-2021 To 13-11-2021	1 week
6	Parents Teachers Meeting	19-11-2021	1 Day
7	Submission of 1st Mid Term Exam Marks to University on or before	20-11-2021	
8	2 nd Spell of Instructions	15-11-2021 To 08-01-2022	8 weeks
9	2nd Mid Term Examinations	10-01-2022 To 18-01-2022	1 week
10	Preparation Holidays & Practical Examinations	19-01-2022 To 25-01-2022	1 week
11	Submission of 2nd Mid Term Exam Marks to University on or before	25-01-2022	***************************************
12	End Semester Examinations	27-01-2022 To 09-02-2022	2 weeks



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Campus Ph: 9640590999, 9347187999, 8096951507.

Institution academic calendar- 2021-22

B. Tech II Years - I Semester

S.No	Event	Date	Duration
1	Traditional Day	09-10-2021	1Day
2	Dussehra Vacation	11-10-2021 To 16-10-2021	1 week
3	Commencement of I semester class work	18-10-2021	
4	Commencement of class work	18-10-2021 To 11-12-2021	8 week
5	1st Mid Term Examinations	13-12-2021 To 18-12-2021	1 week
6	Submission of First Mid Term Exam Marks to the University on or before	24-12-2021	***************************************
7	2 nd Spell of Instructions	20-12-2021 To 12-02-2022	8 week
8	2nd Mid Term Examinations	14-02-2022 To 19-02-2022	1 week
9	Preparation Holidays & Practical Examinations	21-02-2022 To 26-02-2022	1 week
10	Submission of 2 nd Mid Term Exam Marks to University on or before	26-02-2022	
11	End Semester Examinations	28-02-2022 To 12-03-2022	2 week



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Institution academic calendar- 2021-22

B. Tech I Years - I Semester

S.No	Event	Date	Duration
1	Induction programme	09-12-2021 To 18-12-2021	***************************************
2	Commencement of class work	20-12-2021 To 12-02-2022	8 week
3	1st Mid Term Examinations	14-02-2022 To 19-02-2022	1 week
4	Submission of First Mid Term Exam Marks to the University on or before	26-02-2022	***************************************
5	2 nd Spell of Instructions	21-02-2022 To 23-04-2022	9 week
6	2nd Mid Term Examinations	25-04-2022 To 30-04-2022	1 week
7	Preparation Holidays & Practical Examinations	02-05-2022 To 07-05-2022	1 week
8	Submission of 2 nd Mid Term Exam Marks to University on or before	07-05-2022	
9	End Semester Examinations	09-05-2022 To 21-05-2022	2 week



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Institute Academic Calendar 2021-22

B. Tech III, IV Years - II Semester

S.No	Event	Date	Duration
01	Commencement of II semester class work	10-02-2022	
02	Commencement of class work	10-02-2022 To 06-04-2022	8 Weeks
03	1st Mid Term Examinations	07-04-2022 To 13-04-2022	1 Weeks
04	Submission of 1st Mid Term Exam Marks to University on or before	20-04-2022	
05	2 nd Spell of Instructions	16-04-2022 To 24-06-2022	10 Weeks
06	Summer Vacation	09-05-2022 To 21-05-2022	2 Weeks
07	2nd Mid Term Examinations	25-06-2022 To 01-07-2022	1 Weeks
08	Preparation Holidays & Practical Examinations	02-07-2022 To 09-07-2022	1 Weeks
09	Submission of 2nd Mid Term Exam Marks to University on or before	09-07-2022	
10	End Semester Examinations	11-07-2022 To 23-07-2022	2 Weeks



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Institute Academic Calendar 2021-22

B. Tech II Years - II Semester

S.No	Event	Date	Duration
01	Commencement of II semester class work	14-03-2022	
02	Commencement of class work	14-03-2022 To 28-05-2022	11 Weeks
03	Summer Vacation	09-05-2022 To 21-05-2022	2 Weeks
04	1st Mid Term Examinations	30-05-2022 To 04-06-2022	1 Weeks
05	Submission of 1st Mid Term Exam Marks to University on or before	11-06-2022	************
06	2 nd Spell of Instructions	06-06-2022 To 30-07-2022	8 Weeks
07	2nd Mid Term Examinations	01-08-2022 To 06-08-2022	1 Weeks
08	Preparation Holidays & Practical Examinations	09-08-2022 To 16-08-2022	1 Weeks
09	Submission of 2nd Mid Term Exam Marks to University on or before	16-08-2022	
10	End Semester Examinations	17-08-2022 To 30-08-2022	2 Weeks



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Institute Academic Calendar 2021-22

B. Tech I Years - II Semester

S.No	Event	Date	Duration
01	Commencement of II semester class work	23-05-2022	
02	Commencement of class work	23-05-2022 To 16-07-2022	8 Weeks
03	1st Mid Term Examinations	18-07-2022 To 23-07-2022	1 Weeks
04	Submission of 1st Mid Term Exam Marks to University on or before	30-07-2022	
05	2 nd Spell of Instructions	26-07-2022 To 17-09-2022	8 Weeks
06	2nd Mid Term Examinations	19-09-2022 To 24-09-2022	1 Weeks
07	Preparation Holidays & Practical Examinations	26-09-2022 To 01-10-2022	1 Weeks
08	Submission of 2nd Mid Term Exam Marks to University on or before	01-10-2022	
09	End Semester Examinations	03-10-2022 To 18-10-2022	· 2 Weeks



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SRI INDU INSTITUTE OF ENGINEERING AND TECHNOLOGY DEPARTMENT OF CIVIL ENGINEERING

Department Vision:

To become a school of excellence that brings out civil engineers with high technical competencies and promotes high-end research to meet the current and future challenges in civil engineering.

Department Mission:

DM1: To produce civil engineers of high caliber, technical skills and ethical values to serve the society and nation.

DM2: By providing infrastructure and state-of-the-art laboratory facilities to meet the requirements of curriculum and research and consultancy services

DM3: To develop strong linkage with relevant industries, institutes of higher learning and reputed research organizations.

DM4: To encourage students to pursue higher education and take competitive exams and various career enhancing courses.

Head of

Department

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Sn Indu Institute of Engineering & Teos Chil Engo Dept.

Sheriguda(V), Ibrahimpatration av. Brancher, R.R. Div. 501 511.

R.R. Dist. Telangana -601 510



Sheriguda (V), Ibrahimpatnam (M), Hyderabad, R.R. Dist., Telangana State - 501 510.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DEPARTMENT VISION

To become prominent knowledge hub for learners, strive for educational excellence with innovative and industrial techniques so as to meet the software industry needs.

DEPARTMENT MISSION

- DM1 To Provide educational ambience that enhances innovations, problem solving skills, leadership qualities, decision making, teamspirit and ethical responsibilities.
- DM2 Imparting quality education with professional and personal ethics, so as to attain with challenging technological needs of industry and society.
- DM3 To provide academic infrastructure and develop linkage with the world class organizations to strengthen industry-academia relationships for learners.
- DM4 Provide platform to strengthen novel research in thrust area of Computer Science and Engineering to serve the needs of Government and Society.

Head of the Department Computer Science & Engg. Dept. SRI INDU INSTITUTE OF ENGG & TECH. Shelpida(V), Erahirrichani/M), R.R.Dhi/501 510.

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Shenguda(V), (brahimpatnam: R.R Dist. Telangana -501 510



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

VISION AND MISSION

VISION:

To become a recognized center in the field of Electronics and Communication Engineering by producing creative engineers with social responsibility and address everchanging global challenges.

MISSION:

DM1: To facilitate an academic environment that enables student's centric learning.

DM2: To provide state-of-the-art hardware and software technologies to meet industry requirements.

DMs: To continuously update the Academic and Research infrastructure.

DM4: To Conduct Technical Development Programs for overall professional caliber of Stake Holders.

Head of the Department

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Department of Civil Engineering

Academic Calendar -2021-22

B. Tech III, IV Years - I Semester

S.No	Event	Date	Duration
1	Commencement of I semester class work	06-09-2021	
2	Commencement of class work	06-09-2021 To 06-11-2021	9 weeks
3	Traditional Day	09-10-2021	1Day
4	Dussehra Vacation	11-10-2021 To 16-10-2021	1 week
5	1st Mid Term Examinations	08-11-2021 To 13-11-2021	1 week
6	Parents Teachers Meeting	19-11-2021	1 Day
7	Submission of 1st Mid Term Exam Marks to University on or before	20-11-2021	
8	2 nd Spell of Instructions	15-11-2021 To 08-01-2022	8 weeks
9	2nd Mid Term Examinations	10-01-2022 To 18-01-2022	1 week
10	Preparation Holidays & Practical Examinations	19-01-2022 To 25-01-2022	1 week
11	Submission of 2nd Mid Term Exam Marks to University on or before	25-01-2022	*************
12	End Semester Examinations	27-01-2022 To 09-02-2022	2 weeks

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Department of Civil Engineering

Academic Calendar -2021-22

B. Tech II Years - I Semester

S.No	Event	Date	Duration
1	Traditional Day	09-10-2021	1Day
2	Dussehra Vacation	11-10-2021 To 16-10-2021	1 week
3	Commencement of I semester class work	18-10-2021	
4	Commencement of class work	18-10-2021 To 11-12-2021	8 week
5	1st Mid Term Examinations	13-12-2021 To 18-12-2021	1 week
6	Submission of First Mid Term Exam Marks to the University on or before	24-12-2021	
7	2 nd Spell of Instructions	20-12-2021 To 12-02-2022	8 week
8	2nd Mid Term Examinations	14-02-2022 To 19-02-2022	1 week
9	Preparation Holidays & Practical Examinations	21-02-2022 To 26-02-2022	1 week
10	Submission of 2 nd Mid Term Exam Marks to University on or before	26-02-2022	***************************************
11	End Semester Examinations	28-02-2022 To 12-03-2022	2 week

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Department of Civil Engineering

Academic Calendar -2021-22

B.Tech III, IV Years - II Semester

S.No	Event	Date	Duration
01	Commencement of II semester class work	10-02-2022	
02	Commencement of class work	10-02-2022 To 06-04-2022	8 Weeks
03	1st Mid Term Examinations	07-04-2022 To 13-04-2022	1 Weeks
04	Submission of 1st Mid Term Exam Marks to University on or before	20-04-2022	
05	2 nd Spell of Instructions	16-04-2022 To 24-06-2022	10 Weeks
06	Summer Vacation	09-05-2022 To 21-05-2022	2 Weeks
07	2nd Mid Term Examinations	25-06-2022 To 01-07-2022	1 Weeks
80	Preparation Holidays & Practical Examinations	02-07-2022 To 09-07-2022	1 Weeks
09	Submission of 2nd Mid Term Exam Marks to University on or before	09-07-2022	***************************************
10	End Semester Examinations	11-07-2022 To 23-07-2022	2 Weeks
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Department of Civil Engineering

Academic Calendar -2021-22

B.Tech II Years - II Semester

S.No	Event	Date	Duration
01	Commencement of II semester class work	14-03-2022	
02	Commencement of class work	14-03-2022 To 28-05-2022	11 Weeks
03	Summer Vacation	09-05-2022 To 21-05-2022	2 Weeks
04	1st Mid Term Examinations	30-05-2022 To 04-06-2022	1 Weeks
05	Submission of 1st Mid Term Exam Marks to University on or before	11-06-2022	
06	2 nd Spell of Instructions	06-06-2022 To 30-07-2022	8 Weeks
07	2nd Mid Term Examinations	01-08-2022 To 06-08-2022	1 Weeks
08	Preparation Holidays & Practical Examinations	09-08-2022 To 16-08-2022	1 Weeks
09	Submission of 2nd Mid Term Exam Marks to University on or before	16-08-2022	
10	End Semester Examinations	17-08-2022 To 30-08-2022	2 Weeks

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SKI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY COMPUTER SCIENCE AND ENGINEERING DEPARTMENT TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-A

Semester: I

LH. NO: A-301

W.E.F. 18-10-2021

Period/	1	1 2 3 4		1		1	r. 10-10-202	
Day	9:40-10:30	10:30- 11:20	11:20-12:10	12:10-1:00	1:00- 1:30	1:30-2:20	2:20-3:10	3:10-4:00
Monday	DS	COA	C++ LAB (BA A&DE LAB (B	ATCH-I)/		A&DE	C++	SPORTS
Tuesday	A&DE	DS LAB	(BATCH-I)/ITWS LAB	(BATCH-II)	L	COA/DS(T)	601	Shipperson
Wednesday	DS/C++(T)	COSM	CO-C/SS/I	Charles and the same of the sa	U	C++ LAB (BAT	CH-II)/	DS DS
Thursday	COSM	A&DE	DS	INT	N	A&DE LAB (BA		
Friday	COA	C++			Н	DS LAB(BATCH	-II)/ITWS LAB	BATCH-I)
	ASSESSES.	39/8/2015/	C++/COSM(T) COUN		11	C++	GS I	.AB
Saturday	C++	COSM	DS/COA(T)	LIB		COSM/A&DE(T)	DS	COA

(T) – Tutorial (concern faculty)

Subject Code	Subject 1	Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS301ES	Analog and Digita	al Electronics	Ms. A Apsara	CS308PC	IT Workshop Lab	Mrs.S.Akhila/ Mr A Shiva Shankar
CS302PC	Data Struc	CONTR	Mrs.N.Shilpa	CS309PC	C++ Programming Lab	Ms.G.S.Sravanthi/ Mrs R Padma
MA303BS	Computer Oriente Method	ds	Mrs.T.Revathamma	MC309	Gender Sensitization Lab	Mr.T.Ramesh
CS304PC	Computer Organ Architect	ture	Mrs.S.Akhila		CO-C/SS/DAA	Mrs E Rupa
CS305PC	Object Oriented P Using C	rogramming ++	Ms.G.S.Sravanthi		Sports	Mr P Dinakar
CS306ES	Analog and Digita Lab	l Electronics	Ms.Apsara	Internet	Internet	Mrs B Rajeshwari
CS307PC	Data Structur	es Lab	Mrs.N.Shilpa/ Mrs K Anusha	LIB	Library	Mrs.K.Manmadha
lass In-Charge	: Mrs.S.Akhila			COUN	Counselling	Mrset Roth

Class-Incharge



SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-B

Semester: I

LH. NO: A-302

W.E.F. 18-10-2021

Period/	1	2	3	4	1:00-	5	6	7
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00
Monday	COA	C++/A&DE(T)	DS	A&DE	1100	COSM		S LAB
Tuesday	C++	COA	C++ LAB(BATCH-II)/ A&DE LAB(BATCH-I)		L	A&DE	C++	SPORTS
Wednesday	COSM	ITWS LAB(I	BATCH-I)/ DS LAB	The second secon	U	A&DE/DS(T)	CO-C	/SS/DAA
Thursday	DS	THE RESERVE THE PERSON NAMED IN COLUMN TWO IS NOT THE PERSON NAMED IN COLUMN TWO IS NAMED IN COLUMN TWIND TWO IS NAMED IN COLUMN TWO IS NAMED IN COLUMN TWO IS NAMED IN	BATCH-II)/DS LAB(N	CN	INT	
Friday	A&DE/COS M(T)	COUN	C++ LAB(BATCH-I)/ A&DE LAB(BATCH-II)		C H	LIB	COA	DS COSM/C++(T)
Saturday	A&DE	COSM/COA(T)	COSM	DS		C++	GS	S LAB

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS301ES	Analog and Digital Electronics	Ms. A Apsara	CS308PC	IT Workshop Lab	Mr A Vijay Kumar/ Mr U Naresh
CS302PC	Data Structures	Ms.D Swapna	CS309PC	C++ Programming Lab	Ms.G.S.Sravanthi/ Mrs G.Swapna
MA303BS	Computer Oriented Statistical Methods	Mrs.Ramadevi	MC309	Gender Sensitization Lab	Mrs.Laxmi Shilpa
CS304PC	Computer Organization and Architecture	Mrs.S.Akhila		CO-C/SS/DAA	Mr.A.Shiva Shankar
CS305PC	Object Oriented Programming Using C++	Ms.G.S.Sravanthi	Internet	Internet	Mrs G.Swapna
CS306ES	Analog and Digital Electronics Lab	Mrs.A.Sneha	COUN	Counselling	Ms.G.S.Sravanthi
CS307PC	Data Structures Lab	Ms.D Swapna/ Mrs.K.Manmadha	LIB	Library	Mr.A.Shiva Shankar
L C1	rge : Ms.G.S.Sravanthi		SPORTS	Sports	Mr. K Veera Kishore

Class In-Charge

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-C

Semester: I

LH. NO: A- 213

W.E.F. 18-10-2021

Period/	1	2	3	4	1:00-	5	6	7		
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00		
Monday	COA	A&DE	GS	LAB	1	COA	DS	COUN		
Tuesday	C++	LIB	COA	DS	1 1	DS/C++(T)	C++/COSM(T)	DS		
Wednesday	A&DE	COA	C++ LAB(BATCH-I)/ A&DE LAB(BATCH-II)				U		ATCH-I)/ITWS LAB (
Thursday	C++	COUN		C ++ LAB(BATCH-II)/ A&DE LAB(BATCH-I)		DS	COSM/A&DE(T)	COSM		
Friday	COSM	A&DE	INT	COA	H	C++	GS LA	R		
Saturday	COSM/DS(T)	C++	CO-C/SS/DAA			The second second	TCH-II)/ITWS LAB			

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS301ES	Analog and Digital Electronics	Mrs. B Jyothirmai	CS308PC	IT Workshop Lab	Mr P Babu/ Mr K Veera Kishore
CS302PC	Data Structures	Mrs.N Mounika	CS309PC	C++ Programming Lab	Mrs.P.H.Swarna Rekha/ Mrs N Shilpa
MA303BS	Computer Oriented Statistical Methods	Mr.N.Chandrashekar	MC309	Gender Sensitization Lab	Mr.T.Ramesh
CS304PC	Computer Organization and Architecture	Dr C Thirumalai Selvan	LIB	Library	Mr.K.Veera Kishore
CS305PC	Object Oriented Programming Using C++	Mrs.P.H.Swarna Rekha		CO-C/SS/DAA	Mr.K.Veera Kishore
CS306ES	Analog and Digital Electronics Lab	Mrs.Jyothirmai	INT	Internet	Ms S Anitha
CS307PC	Data Structures Lab	Mrs.N Mounika/ Mrs J Pujitha	COUN	Counselling	Ms S Amiglas
lass In-Charge	Mrs.P.H.Swarna Rekha			/	STREET STO

Class In-Charge

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COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-A

Semester: I

LH. NO: A-311

W.E.F: 06-09-2021

Period/ Day	1	2	3	4	1:00-	5	6	7
	9:40-10:30 10:30-11:20 11:20-12:10 12:10-1:00	1:30	1:30-2:20	2:20-3:10	3.10 4.00			
Monday	FLAT	SE LAB(BA	TCH-I) / ACS LAB(I			WT	PPL	3:10-4:00 INT
Tuesday	IPR	CN	DDB	PPL		SE	SE/FLAT(T)	IPR
Wednesday	SE	WT	CO-C/SS	S/DAA	Ü	ACS LAB(BATCH		
Thursday	DDB	CN	LIB	PPL	N C	WT	FLAT	SPORTS
Friday	WT	CN&WT LAB	(BATCH-I)/ SE LAB	(BATCH-II)	н	SE	WT/CN(T)	FLAT
Saturday	CN	IPR	FLAT/SE(T)	FLAT		DDB	CN	CN/WT(T)

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Code Subject Name		Subject Name	Name of the Faculty	
CS501PC	Formal Lannguage and Automata Theory	Mrs. Y Sunitha	CS506PC	Computer Networks& Web Technologies Lab	Mr. R.Yadgiri Rao / Mrs.P H SwarnaRekha/ Mr.A Vijay Kumar	
CS502PC	Software Engineering	Mrs.J.Pujitha	EN508HS	Advance communication skill lab	Mrs D.Srivani	
CS503PC	Computer Networks	Mr. R.Yadgiri Rao	MC510	Intellectual Property Rights	Mr Sannala Srinivas	
CS504PC	Web Technologies	Mrs.P H Swarna Rekha	INT	Internet	The state of the s	
CS515PE	Principles of Programming Languages (PE-I)	Mrs.E Rupa	LIB	Library	Mrs R Sravanthi Mrs.K.Hephsibah	
CS524PE	Distributed Database (PE-II)	Mrs.N.Mounika		CO-C/SS/DAA	Entracement Torrest CII	
CS505PC	C Software Engineering lab Mrs.J.Pujitha/ Mrs.G.Swapna			Sports	Mrs.K Anusha Mr P Babu	

Class In-charge: Y.Sunitha

Class In-Charge

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COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-B

Semester: I

LH. NO: A-310

W.E.F: 06-09-2021

Period/	1	2	3	4	1:00-	5	6	-
Day	9:40-10:30	10:30-11:20 11:20-12:10 12:10-1	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00	
Monday	SE	CN	FLAT	PPL		DDB		SS/DAA
Tuesday	FLAT	SE LAB(BA	TCH-I)/ACS LAB(E	BATCH-II)	1 . 1	CN	INT	
Wednesday	WT	CN/WT(T)	CN	FLAT	L	FLAT/SE(T)		SPORTS
Thursday	PPL	WT	SE/FLAT(T)	SE	N	ACS LAB(BATCH-I)/CN&WT LAB(BATCH		
Friday	CN	IPR	WT	LIB	CH	PH - PH		
Saturday	DDB	CN&WT LAB	B (BATCH-I)/SE LAB (BATCH-II)			PPL	IPR	WT/CN(T) SE

(T) – Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
CS501PC	Formal Lannguage and Automata Theory	Mrs Y Sunitha	CS506PC	Computer Networks& Web Technologies Lab	Ms M.Swarnalatha&	
CS502PC	Software Engineering	Mrs.A Sudha	EN508HS Advance communication skill		M.Sruthi/Mr P Sriramulu Mrs D.Srivani	
CS503PC	Computer Networks	Ms M.Swarnalatha	MC510	Intellectual Property Rights		
CS504PC	Web Technologies	Mrs M.Sruthi	INT	Internet	Mr Sannala Srinivas	
CS515PE	Principles of Programming Languages (PE-I)	Mr A.Shiva Shankar	I I I	CO-C/SS/DAA	Mrs K Hephsibah Mrs J Soulatha	
CS524PE	Distributed Database (PE-II)	Mr.U Naresh	LIB	Library	Mrs K Manmadha	
CS505PC	Software Engineering lab	Mrs.A Sudha/Mrs J Soulatha	SPORTS	Sports	Mr P Babu	

ass in-charge: M.Swarnalatha

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-C

Semester: I

LH. NO: A-315

W.E.F: 06-09-2021

Period/	1	2	3	4	1.00	5	-	1 -	
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:00-	1:30-2:20	2:20-3:10	3:10-4:00	
Monday	DDB	PPL	WT	CN		FLAT/SE(T)	IPR	FLAT	
Tuesday	PPL	CN	WT	SE	L	CN&WT LAB(BATCH-I)/SE LAB(BATCH-			
Wednesday	CN	SE LAB(BA	SE LAB(BATCH-I)/ ACS LAB (BATCH-II)			SE	FLAT	CN/WT(T)	
Thursday	SE	FLAT	INT	IPR	N C	SE/FLAT(T)	LIB	DDB	
Friday	WT	CO-C/SS	/DAA	IPR	н			CH-I)/CN&WT LAB(BATCH-II)	
Saturday	FLAT	WT/CN(T)	SE	PPL	WT		DDB	SPORTS	

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS501PC	Formal Lannguage and Automata Theory	Dr.G.Narayana	CS506PC	Computer Networks& Web Technologies Lab	Ms M.Swarnalatha& M.Sruthi/Mrs K Anusha
CS502PC	Software Engineering	Mrs. M.Rajya Lakshmi	EN508HS	Advance communication skill lab	Mrs D.Srivani
CS503PC	Computer Networks	Ms M.Swarnalatha	MC510	Intellectual Property Rights	Mr Sannala Srinivas
CS504PC	Web Technologies	Mrs M.Sruthi	INT	Internet	Mrs E Rupa
CS515PE	Principles of Programming Languages (PE-I)	Mrs E.Rupa	LIB	Library	Mrs K Hephsibah
CS524PE	Distributed Database (PE- II)	Mr A.Shiva Shankar		CO-C/SS/DAA	Mrs Y Sunitha
CS505PC	Software Engineering lab	Mrs. M.Rajya Lakshmi/ Mr.P.Babu		sports	Mr U Naresh

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COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-A

Semester: I

LH. NO: A-301

W.E.F: 06-09-2021

Period/	1	2	3 11:20-12:10	4	1:00- 1:30	5	6	7
Day	9:40-10:30	10:30-11:20		12:10-1:00		1:30-2:20	2:20-3:10	3:10-4:00
Monday	C&NS	DM	C&NS/CC(T)	POE		DM	CO-C/S	0.0000000000000000000000000000000000000
Tuesday	CC	POE	C&NS	RTS	L	DM	C&NS	LIB
Wednesday	DM	MAJO	OR PROJECT (STA	ũ	MA	JOR PROJECT (STA	10000	
Thursday	POE	DM	RTS	INT	C	MINI PROJECT		
Friday	RTS	CC	SEMIN	SEMINAR		POE	RTS/C&NS(T)	СС
Saturday	C&NS		C&NS LAB			CC	CC/RTS(T)	SPORTS

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS701PC	Cryptography& Network Security	Mrs. Ch Sai Vijaya	CS705PC	Seminar Coordinator	Mrs.Y.Sunitha/Mrs E Rupa
CS702PC	Data Mining	Mr. K Veera Kishore	CS706PC	Major Project (Stage-I)	Dr.C. ThirumalaiSelvan/ Dr L Kartheesan
CS714PE	Cloud Computing (PE-IV)	Ms.S Anitha	INT	Internet	Mrs K Manmadha
CS722PE	Real Time Systems (PE-V)	Mrs R Padma	LIB	Library	Mrs M Rajya Lakshmi
	Principles of Entrepreneurship (OE-II)	Mr N.B.C Sidhu		CO-C/SS/DAA	Mrs M Karuna
CS703PC	Cryptography& Network Security Lab	Mrs. Ch SaiVijaya/ Mr K Veera Kishore		Sports	Mr U Naresh
CS704PC	Mini Project Coordinator	Dr L Kartheesan/ Dr.C.Thirumalai Selvan			

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COMPUTER SCIENCE AND ENGINEERING & TECHNOLOGY

TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-B

Semester: I

LH. NO: A-316

W.E.F: 06-09-2021

Period/	1	2	3	4		5	6	7
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10- 1:00	1:00- 1:30	1:30-2:20	2:20-3:10	3:10-4:00
Monday	POE	CC	CC/C&NS(T)	DM		MINI PROJECT		
Tuesday	DM	RTS	СС	C&NS	,	LIB C&NS/RTS(T)		DM
Wednesday	RTS/C&NS(T)	RTS	POE	СС	Ü	C&NS LAB		
Thursday	cc	MAJOR	PROJECT (STAGE	E-I)	N C	MAJOR PROJECT (STAGE-		
Friday	DM	INT	RTS	C&NS	H	RTS SEMINAR		
Saturday	POE	CC	CO-C/SS/E	DAA		C&NS POE SPOR		

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS701PC	Cryptography& Network Security	Mrs. CH SaiVijaya	CS705PC	Seminar Coordinator	Y.Sunitha/ Mrs E Rupa
CS702PC	Data Mining	Mrs.B.Rajeshwari	CS706PC	Major Project (Stage-I)	Dr.C.Thirumalai Selvan/ Dr L Kartheesan
CS714PE	Cloud Computing (PE-IV)	Mrs.R.Padma	INT	Internet	Mrs G Swapna
CS722PE	Real Time Systems (PE-V)	Mr.P.Babu	LIB Library		Mrs J Pujitha
	Principles of Entrepreneurship (OE-II)	Mr N.B.C Sidhu		CO-C/SS/DAA	Mrs R Sravanthi
CS703PC	Cryptography& Network Security Lab	Mrs.CH.Sai Vijaya/ Mrs K. Hephsibah		Sports	Mr P Dinakar
CS704PC	Mini Project Coordinator	Dr L Kartheesan/ Dr.C.Thirumalai Selvan			

Class In-charge: CH. Sai Vijaya

Class In-Charge

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY COMPUTER SCIENCE AND ENGINEERING DEPARTMENT

TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-C

Semester: I

LH. NO: A- 101

W.E.F: 06-09-2021

Period/	1	2	3	4	1:00-	5	6	7
Day	9:40-10:30	10:30-11:20 11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00	
Monday	RTS		MINI PROJECT			C&NS	DM(T)	RTS
Tuesday	C&NS	CC	DM	POE	L	C&NS LAB		
Wednesday	CC	POE	LIB	RTS		DM CO-C/SS/DAA		S/DAA
Thursday	DM	POE	CC	POE(T)	N C	RTS	C&NS(T)	SPORTS
Friday	DM	MA.	MAJOR PROJECT (STAGE-I)				PROJECT (ST.	
Saturday	C&NS	INT	CC	C&NS	1 1	CC(T)	SEMINAR	

(T) – Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS701PC	Cryptography& Network Security	Mrs.G.Sravanthi	CS705PC	Seminar Coordinator	Y.Sunitha/ Mrs E Rupa
CS702PC	Data Mining	Mrs.B.Rajeshwari	CS706PC Major Project (Stage-I)		Dr.C.Thirumalai Selvan/ Dr L Kartheesan
	Cloud Computing (PE-IV)	Mrs.R.Padma	INT	Internet	Mrs D Swapna
CS722PE	Real Time Systems (PE-V)	Mr.P.Babu	LIB Library		Mrs A Sudha
	Principles of Entrepreneurship (OE-II)	Mr N.B.C Sidhu		CO-C/SS/DAA	Mrs R Sravanthi
CS703PC	Cryptography& Network Security Lab	Mrs.G.Sravanthi		Sports	Mr P Sreeramulu
CS704PC	Mini Project Coordinator	Dr L Kartheesan/ Dr.C.Thirumalai Selvan			

Class In-charge: Mrs.B.Rajeshwari

Class In-Charge

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Khalsa Ibrahimpatnam, Sheriguda(V), Ibrahimpatnam(M), Ranga Reddy Dist., Telangana – 501 510

Website: https://siiet.ac.in/

TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-A

Semester: II

LH. NO: A-301

W.E.F. 21-03-2022

Period/	1	2	3	4	1:00-	5	6	7
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00
Monday	os	BEFA	BEFA DBMS DM			OS LAB(BATCH-I)/JAVA LAB(BATCH-II		
Tuesday	BEFA	COI	DM	os	L	JAVA	os	DBMS
Wednesday	JAVA	JAVA LAB(I	JAVA LAB(BATCH-I)/DBMS LAB (BATCH-II)			COUN	JAVA	os
Thursday	DBMS	DM	COI	BEFA	C	os	JAVA	SPORTS
Friday	DM	DM	DM LIB BEFA			OS LAB(BATCH-II)/DBMS LAB(BATCH-I)		
Saturday	COI	INT	JAVA	DBMS		DBMS CO-C/SS/DAA		

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
CS401PC	Discrete Mathematics	Mr.B.Mahesh	CS408PC	Java Programming Lab	Mrs.N.Shilpa/ Mrs.J.Pujitha	
SM402MS	Business Economics & Financial Analysis	Mr.U.Fanidara Baradwaj	*MC409	Constitution of India	Mr.K.Bala Krishna	
CS403PC	Operating Systems	Dr.L.Kartheesan	LIB	Library	Mrs R Sravanthi	
CS404PC	Database Management Systems	Mrs.S.Akhila	INT	Internet	Mrs.R.Padma	
CS405PC	Java Programming	Mrs.N.Shilpa		CO-C/SS/DAA	Mr.P.Dinakar	
CS406PC	Operating Systems Lab	Dr.L.Kartheesan/ Mrs.K.Anusha		Sports	Mr.K.Veera Kishore	
CS407PC	Database Management Systems Lab	Mrs.S.Akhila/ Mr.P.Dinakar		Counselling Mr.P.B		

Class Incharge

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Khalsa Ibrahimpatnam, Sheriguda(V), Ibrahimpatnam(M), Ranga Reddy Dist., Telangana – 501 510

Website: https://siiet.ac.in/

TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-B

Semester: II

LH. NO: A-302

W.E.F. 21-03-2022

Period/	1	2	3	4	1:00-	5	6	7
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00
Monday	os	DM	JAVA	LIB	1100	BEFA	COI	
Tuesday	DM	JAVA LAB(B	ATCH-I)/DBMS LAI		1 , 1	BEFA		DBMS
Wednesday	BEFA	JAVA OS DBMS			T U		OS H-II)/DBMS LAB(I	SPORTS
Thursday	DM	os	DBMS	COI	N -	COUN	1	
Friday	JAVA	os	COI	JAVA	H	DBMS	JAVA	DM
Saturday	DBMS	OS LAB(BA	RATCH-IVIAVA I AB/DATCH ID				INT	S/DAA DM

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
CS401PC	Discrete Mathematics	Mr.B.Mahesh	CS408PC	Java Programming Lab	Dr.C.Thirumalai Selvan/
SM402MS	Business Economics & Financial Analysis	Mr.U.Fanidara Baradwaj	*MC409 Constitution of India		Mrs R PAdma Mr.K.Bala Krishna
CS403PC	Operating Systems	Ms.S.Anitha	INT Internet		Mrs.R.Padma
CS404PC	Database Management Systems	Mrs.P.H.Swarna Rekha	LIB	Library	Mrs J Soulatha
CS405PC	Java Programming	Dr.C.Thirumalai Selvan		CO-C/SS/DAA	
CS406PC	Operating Systems Lab	Ms.S.Anitha/ Mrs K Hephsibah		Sports	Mrs.R.Padma Mr.K.Veera Kishore
CS407PC	Database Management Systems Lab	Mrs.P.H.Swarna Rekha/ Mrs.B.Rajeshwari		Counselling	Mr.P.Dinakar
Class In-Cha	Class In-Charge : Ms.S.Anitha Mentor 1: Ms		,	Mentor 2: Mrs.R.Padma	

Class In-Charge

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Khalsa Ibrahimpatnam, Sheriguda(V), Ibrahimpatnam(M), Ranga Reddy Dist., Telangana – 501 510

Website: https://siiet.ac.in/

TIME TABLE FOR AY-2021-22

Class: II-B.Tech CSE-C

Semester: II

LH. NO: A-307

W.E.F. 21-03-2022

Period/	1	2	3	4	1:00-			1	
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	7503637	1 20 2 20	- 6	7	
Monday	DBMS				1:30	1:30-2:20	2:20-3:10	3:10-4:00	
-		JAVA LAB(I	BATCH-I)/DBMS LAI		DM	os	COUN		
Tuesday	os	DM	COI	BEFA	L	JAVA	DBMS		
Wednesday	DM	DBMS	COI	JAVA	U	DM		SPORTS	
Thursday	BEFA	CO-C/SS/DAA			- N	JATA		BEFA	
Friday	JAVA			os	C	OS LAB(BAT	TCH-II)/DBMS LAB(BATCH-I)		
	The second secon	LIB	OS	JAVA	н	os	DM	DBMS	
Saturday	BEFA	DBMS	INT	COI		OS LAB(BAT	CH-I)/JAVA LAB(I		

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
CS401PC	Discrete Mathematics	Mr.B.Mahesh		Java Programming Lab	Mrs.D.Swapna/	
SM402MS	Business Economics & Financial Analysis	Mr.U.Fanidara Baradwaj	*MC409	Constitution of India	Mrs.J.Soulatha Mr.K.Bala Krishna	
CS403PC	Operating Systems	Ms.S.Anitha	LIB	Library	Mrs K Manmadha	
CS404PC	Database Management Systems	Mrs.P.H.Swarna Rekha	INT	Internet	Mrs.R.Padma	
CS405PC	Java Programming	Mrs.D.Swapna		CO-C/SS/DAA		
CS406PC	Operating Systems Lab	Ms.S.Anitha/ Mrs K Anusha		Sports	Mr.P.Dinakar Mr.K.Veera Kishore	
CS407PC	Database Management Systems Lab	Mrs.P.H.Swarna Rekha/Mrs.M.Karuna		Counselling	Mr.P.Dinakar	

Class In-Charge Mrs.P.H.Swarna Rekha

Class In Charge

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TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-A

Semester: II

LH. NO: A-201

W.E.F. 03-03-2022

Period/	1	2	3	1	1:00-			
Day	9:40-10:30	10:30-11:20	11:20-12:10	12.10.1.00	-	5	6	7
Monday		2.30.22.2	11.20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00
*	DAA	FIOT	ML	LIB/ ES(L.E)		STM	FIOT	DAA/CD(T)
Tuesday	STM	ML	DAA	STM	1 🕫 🖯			
Wednesday	ML	CD	FIOT		L	ML LAB(BATCH-I &II)		
-			FIOT	STM	N	DAA	COUN	CD
Thursday	CD	CD LAB(B	ATCH-I)/STM LAB(BATCH-II)	6	ML	FIOT /DAA/T)	CDODTO
Friday	FIOT	CD/ML(T)	INT/ES(L.E)	DAA	- G	1101/10/11(1)		SPORTS
Saturday	FIOT		19000000	DAA	н	CD LAB(BATCH-II)/STM LAB(BATCH-I)		
Saturday	101	CD	STM	DAA		ML	CO-C/SS/	DAA

(T) - Tutorial (concern faculty)

Subject Name	Name of the Faculty	Subject	Subject Name	Name of the Faculty
Machine Learning	Man Waterin	0.000		
24.55	Mr K Yadgiri Kao	CS605PC	Compiler Design Lab	Mrs. Y. Sunitha/ Mrs. R. Padma
Compiler Design	Mrs.Y,Sunitha	*MC609	Environmental Science	
Software Testing Methodologies	Mrs.E.Rupa	0.0000000000000000000000000000000000000	The state of the s	Mr.G.Vijay
	Continues of the	411	Internet	Mr P Sreeraumulu
Algorithms	Mrs.M.Sruthi	LIB	Library	Mrs.J.Pujitha
Fundamentals of Internet of Things	Mrs.J.Pujitha		CO-C/SS/DAA	Ms G.SSravanthi
Machine Learning Lab	Mr P Babu/		Sports	M- P.D. J
Software Testing Methodologies			op.iii	Mr.P.Dinakar
Lab	Mrs.E.Rupa/Mrs.D.Uma		Counselling	Mrs.M.Karuna
	Machine Learning Compiler Design Software Testing Methodologies Design and Analysis of Algorithms Fundamentals of Internet of Things Machine Learning Lab Software Testing Methodologies	Machine Learning Mr R Yadgiri Rao Compiler Design Software Testing Methodologies Design and Analysis of Algorithms Fundamentals of Internet of Things Mrs. J. Pujitha Mr P Babu/ Mrs K Manmadha Software Testing Methodologies Lab Mrs. E. Rupa/Mrs. D. Uma	Machine Learning Mr R Yadgiri Rao CS605PC Compiler Design Mrs.Y.Sunitha MC609 Software Testing Methodologies Design and Analysis of Algorithms Fundamentals of Internet of Things Mrs.J.Pujitha Mr P Babu/ Mrs K Manmadha Software Testing Methodologies Lab Mrs.E.Rupa/Mrs.D.Uma	Machine Learning Mr R Yadgiri Rao CS605PC Compiler Design Lab Compiler Design Mrs.Y,Sunitha Software Testing Methodologies Design and Analysis of Algorithms Mrs.M.Sruthi Fundamentals of Internet of Things Mrs.J.Pujitha Mr P Babu/ Mrs K Manmadha Software Testing Methodologies Mrs.E.Rupa Mr P Babu/ Mrs K Manmadha Software Testing Methodologies Lab Mrs.E.Rupa/Mrs.D.Uma Counselling

Class-Incharge

Indu Institute of Engineering & IUN Sheriguda(V). Ibrahimpatre (M) CreAL R R Dist. Telengana 50



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TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-B

Semester: II

LH. NO: A- 202

W.E.F. 03-03-2022

							TT TABLE I U	J-03-2022	
Period/	1	2	3	4	1:00-	5	6	7	
Day	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00	
Monday	ML	STM	INT/ES(L.E)	FIOT	1.00	DAA	722	7877	
Tuesday	DAA	DAA/CD(T)	FIOT	ML	1 .		CD	CD	
Wednesday	STM		LLAB(BATCH-I &I	L CD LAB(BATCH-I)/SIM LAB					
Thursday	CD	FIOT			N	STM	ML	DAA	
Friday			FIOT/ML(T)	DAA	C	STM	FIOT	STM	
	ML	LIB/ ES(L.E)	FIOT	COUN	Н	STM/DAA(T)	CO-C/S	S/DAA	
Saturday	CD	CD LAB(BA	TCH-II)/STM LAB(BATCH-I)		ML	DAA	SPORTS	

(T) – Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
CS601PC	Machine Learning	Mr.U.Naresh	CS605PC	Compiler Design Lab	Dr.G.Narayana/	
CS602PC	Compiler Design	Dr.G.Narayana	*MC609	Environmental Science	Mrs.Ch Sai Vijaya	
CS615PE	Software Testing Methodologies	Mrs.D.Uma	INT	Internet	Mr.G.Vijay Mrs.D.Uma	
CS603PC	Design and Analysis of Algorithms	Mrs.M.Sruthi	LIB	Library	Mrs M Sudha	
	Fundamentals of Internet of Things	Mr.A.Shiva Shenkar		CO-C/SS/DAA	Mrs.M.Karuna	
CS604PC	Machine Learning Lab	Mr.U.Naresh/ Mr.P.Babu		Sports	Mr A Vijay Kumar	
CS625PE				Counselling	Mrs.M.Karuna	

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TIME TABLE FOR AY-2021-22

Class: III-B.Tech CSE-C

Semester: II

LH. NO: A- 208

W.E.F. 03-03-2022

Period/	1	2	3	- 1	1.00			5-03-2022
Day	9:40-10:30	10:30-11:20	11.20.12.10	4	1:00-	5	6	7
Monday			11:20-12:10	12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00
-	STM	CD LAB(B	BATCH-I)/STM LAB(I	BATCH-II)		FIOT	ML	
Tuesday	ML	CD	FIOT	CD	1	100000000000000000000000000000000000000		DAA
Wednesday	DAA	STM	LIB/ES(L.E)		- L	DAA	STM	STM
Thursday			LIB/ES(L.E)	CD	N	FIOT/CD(T)	CO-C/SS/	DAA
4	ML	COUN	FIOT	FIOT	C	ML	STM	
Friday	CD	CD/DAA(T)	STM	FIOT	Н Н			SPORTS
Saturday	DAA	ML	INT/ES(L.E)		- ***	ML LAB(BATCH-I & I		
7.00	torial (names of		HVI/EO(L.E)	STM/ML(T)		CD LAB(BAT	TCH-II)/STM LAB(BATCH-D

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty Subject Code		Subject Name	Name of the Faculty	
CS601PC	Machine Learning	March Street Color of Property	Code		Name of the Facult	
	Control of the contro	Mrs.N.Mounika	CS605PC	Compiler Design Lab	Mrs.Y.Sunitha/	
CS602PC	Compiler Design	Mrs.Y.Sunitha			Mrs G Swapna	
CS615PE	Software Testing Methodologies		*MC609	Environmental Science	Mr.G.Vijay	
	- Simplification of the second	Mrs.E.Rupa	INT	Internet	Mrs P Sreeramulu	
CS603PC	Design and Analysis of Algorithms	Mrs.B.Rajeshwari	LIB	Library	Mrs D Swapna	
	Fundamentals of Internet of Things	Mr.K.Veera Kishore		60.000	Mis D Swapiia	
CS604PC		Mrs.N.Mounika/		CO-C/SS/DAA	Mrs M Rajyalakshmi	
100000000000000000000000000000000000000	Machine Learning Lab	Mrs.K.Hepshibah		Sports	Mr S Prudhvi Raj	
CS625PE	Software Testing Methodologies Lab	Mrs.E.Rupa/ Mrs K Manmadha		Counselling	Mrs.M.Karuna	
Class In-Ch	arge: Mrs.E.Rupa	Mentor 1: Mrs.E.Rupa		Mentor 2: Mrs.B.Rajeshwari		

Class In-Charge

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TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-A

Semester: II

LH. NO: A-108

W.E.F. 03-03-2022

Period/	1	2	3 11:20-12:10	4	1:00-	5	- 6	7	
Day	9:40-10:30	10:30-11:20		12:10-1:00	1:30	1:30-2:20	2:20-3:10	3:10-4:00	
Monday	DS	DS	LIB	OB		TQM	TOM	COUN	
Tuesday	OB	TQM	INT	DS	L	TOM			
Wednesday	TQM	OB	CO-C/SS		Ü	DS	OB	SPORTS	
Thursday		PROJECT W	ORK STAGE-II		N	101			
Friday		The second secon	ORK STAGE-II		C	PROJECT WORK STAGE-II			
Saturday				H	PROJECT WORK STAGE-II				
Surany	PROJECT WORK STAGE-II					PROJECT WORK STAGE-II			

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty
SM801MS	Organizational Behaviour	Mrs G Nirmala	INT	Internet	Mrs M Sruthi
CS812PE	Distributed Systems	Mrs.G.S.Sravanthi	LIB	Library	The state of the s
	Total Quality Management	Mr.S.Srinivas	72779	CO-C/SS/DAA	Mr.P.Dinakar
CS802PC	Project Stage - II	Mr.K.Veera Kishore/ Mrs.J.Pujitha/ Mr P Babu		Sports	Mrs N Shilpa Mr.P.Dinakar
				Counselling	Mrs D Swapna

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TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-B

Semester: II

LH. NO:A-109

W.E.F. 03-03-2022

Period/ Day	9:40-10:30	2 10:30-11:20	3 11:20-12:10	4 12:10-1:00	1:00-	5 1:30-2:20	2:20-3:10	7 3:10-4:00
Tuesday	TQM	OB	TQM	COUN	1	DS	DS	OB
Wednesday	DS	INT	TQM	OB	1 11 1	TOM		TQM
Thursday	PROJECT WORK STAGE-II				N H	PROJECT WORK STAGE-II		
Friday		PROJECT WORK STAGE-II				PROJECT WORK STAGE-II		
Saturday		SOMEONIC CONTRACTOR CONTRACTOR		н	manufacture Control Control Control Control Control			
2	PROJECT WORK STAGE-II					PROJECT WORK STAGE-II		

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
SM801MS	Organizational Behaviour	Mrs G Nirmala	INT	Internet	Mr.P.Babu	
CS812PE	Distributed Systems	Mrs.G.S.Sravanthi	LIB	Library	Mr P Babu	
	Total Quality Management	Mr.S.Srinivas		CO-C/SS/DAA	Mrs.K.Saidamma	
CS802PC	Project Stage - II	Mrs.M.Sruthi/ Mrs.A Sudha / /Mrs M Rajyalakshmi		Sports	Mr.P.Dinakar	
C) 1 C)	rrge : Mr P Rabu			Counselling	Mrs J Soulatha	

Class In-Charge : Mr.P.Babu

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Website: https://siiet.ac.in/

TIME TABLE FOR AY-2021-22

Class: IV-B.Tech CSE-C

Semester: II

LH. NO:A-101

W.E.F. 03-03-2022

Period/ Day	9:40-10:30	2 10:30-11:20	3 11:20-12:10	4 12:10-1:00	1:00-	5 1:30-2:20	6 2:20-3:10	7 3:10-4:00		
									Monday	TQM
Tuesday	DS	LIB	OB	TQM	L			COUN		
Wednesday	OB	TQM	OB	TQM	Ū	DS	TQM	SPORTS		
Thursday	PROJECT WORK STAGE-II				- N	INT	DS DS			
Friday		PROJECT WORK STAGE-II					PROJECT WORK STAGE-II			
Saturday						PROJECT WORK STAGE-II				
- 10 10 10 10 10 10 10 10 10 10 10 10 10	1000	TROJECT V	OKK STAGE-II	PROJECT WORK STAGE-II				PROJECT WORK STAGE-II		

(T) - Tutorial (concern faculty)

Subject Code	Subject Name	Name of the Faculty	Subject Code	Subject Name	Name of the Faculty	
SM801MS O	Organizational Behaviour	Mrs G Nirmala	INT	Internet	Mr.P.Babu	
CS812PE	Distributed Systems	Mrs.CH Sai vijaya	LIB	Library		
	Total Quality Management	Mr.S.Srinivas	Litt	CO-C/SS/DAA	Mr.P Sreeramulu	
CS802PC	Project Stage - II	Ms.G.S.Sravanthi / Mr A Shiva Shankar/ Mrs D Swapna		Sports	Mr.P Sreeramulu Mr.P Sreeramulu	
	armer Mrs CU Sai alliano			Counselling	Mr.P Sreeramulu	

Class In-Charge: Mrs.CH Sai vijaya

Class fir-Charge

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Sri Indu Institute of Engineering & Teo: Sheriguda(V), Ibrahimpatham(M) R.R. Dist. Telangana, 501 5.10

Sheriguda (V), Ibrahimpatnam (M), Hyderabad, R.R. Dist., Telangana State-501 510.

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING WORKLOAD 2021-22-I SEM

S.No	FACULTY NAME	SUBJECT	WORK LOAD	LABS AND OTHER ACTIVITIES	WORK	тота
1	Dr.C.THIRUMALAI SELVAN	II-CSE-COA	5	MINI & MAJOR PROJECT CO- ORDINATOR	3,7	15
2	Dr.G.NARAYANA	III-CSE-C-FLAT	5			5
3	Dr.B.G.OBULA REDDY	IV-CSE-C-C&NS	5	IV-CSE-C&NS LAB	6	11
4	Dr L KARTHEESAN			MINI & MAJOR PROJECT CO- ORDINATOR	3,7	10
5	R YADGIRI RAO	III-CSE-A-CN	- 5	III-CSE-A-CN LAB	6	11
6	K. VEERA KISHORE	IV-CSE-A-DM	5	IV-CSE-A-C&NS LAB,II-	6,6	17
7	Y. SUNITHA	III-CSE-A,B-FLAT	5,5	IV-CSE-A,B,C-	2,2,2	16
8	M.SRUTHI	II-CSE-A,B-DS	5,5	II-CSE-B-DS LAB	6	16
9	S.ANITHA	IV-CSE-A,B-CC	5,5	II-CSE-C-INT,II-CSE-C-	1,1	12
10	G. S.SRAVANTHI	III-CSE-A,B-OOPS USING C++	4,5	III-CSE-A,B-OOPS USING C++ LAB,II-CSE B-COUN	6,6,1	22
11	N.SHILPA	II-CSE-A-DS	6	II-CSE-A-DS LAB,II- CSE-C-C++ LAB	6,6	18
12	P H SWARNA REKHA	II-CSE-C-OOPS USING C++,III- CSE-A-WT	5,5	II-CSE-C-OOPS USING C++ LAB,III-CSE-A-WT LAB	6,6	22
13	Ј.Р ИЛТНА	III-CSE-A-SE	5	III-CSE-A-SE LAB,II- CSE-C-DS LAB,IV-CSE- B-LIB	6,6,1	18
14	E.RUPA	IV-CSE-A,C,-PPL	3,3	III-C-INT,II-CSE-A-CO- C/SS/DAA,IV-CSE- A,B,C-SEMINAR CO- ORDINATOR,II-CSE-A-	1,2,2,2,2	15
15	CH.SAI VIJAYA	IV-CSE-A,B-C&NS	5,5	IV-CSE-A,B-C&NS LAB	6,6	22
16	A.SHIVA SHANKAR	III-CSE-B-PPL,III- CSE-C-DDB	3,3	II-CSE-A-ITWS LAB	6	11
17	M.SWARNALATHA	III-CSE-B,C-CN	5,5	III-CSE-B,C-CN LAB	6,6	22
18	S.PRUDHVI RAJ	IV-ECE-C-DCCN	5	IV-ECE-C-DCCN LAB,IV-ECE-A-JAVA LAB	6,6	17

St. Indu Industry in the Property of St. 160

19	U.NARESH	III-CSE-B-DDB	3	UI-CSE-C-SPORTS,IV- CSE-A-SPORTS,II-CSE- B-ITWS LAB,IV-ECE-B- JAVA LAB	1,2,6,6	18
20	S.AKHILA	II-CSE-A,B-COA	5,5	II-CSE-A-ITWS LAB	6	16
21	P.SRIRAMULU	II-CSE-C-COA	5	CSE-SPORTS	6,1	12
22	P.DINAKAR			II-CSE-C-ITWS LAB,IV- CSE-B-SPORTS,IV-ECE- C-JAVA LAB		13
23	P.BABU	I-ECE-A,AI&DS- PPS	6	II-CSE-A-C++ LAB,II- CSE-B-INT,III-CSE-A,B- SPORTS,I-CSE-IOT-PPS	6,1,2,6	21
24	D.UMA	IV-ECE-A,B-DCCN	5,5	LAB	6	16
25	N.MOUNIKA	CSE-A-DDB	5,3	II-CSE-C-DS LAB	6	14
26	B.RAJESHWARI	IV-CSE-B,C-DM	5,5	II-CSE-A-INT	1	11
27	D.SWAPNA	II-CSE-A-DS	5	CSE-C-INT	6,1	12
28	A SUDHA	III-CSE-B-SE	5	CSE-C-LIB	6,1	12
29	M KARUNA		.,	CSE-A-CO-C/SS/DAA	6,2	8
30	A VIJAY KUMAR			CSE-B-ITWS LAB	6,6	12
31	K MANMADHA	IV-CSE-C-CC	5	A-INT,II-CSE-B-DS	1,1,6	13
32	G SWAPNA			CSE-B-C++ LAB	6,6	
33	R PADMA	IV-CSE-B,C-RTS	4,4	II-CSE-A-C++ LAB,II- CSE-B-INT,III-CSE-A,B-	6,1,1,1	12
34	J SOULATHA			CSE-B-CO-C/SS/DAA	6,2	8
35	K ANUSHA			III-CSE-A-CO- C/SS/DAA,III-CSE-C-	2,6,6	14
36	K HEPHSIBAH			CSE-B-CN&NS LAB	3,6	9
37	B S SWAPNA SHANTHI	I-IOT,AI&ML-PPS	6,6	LAB	6,6	24
38	M RAJYALAKSHMI	III-CSE-C-SE	5	CSE-A-LIB	6,1	12
39	R SRAVANTHI	I-CSE-AI&ML-PPS	6	III-CSE-A-INT,IV-CSE- B,C-CO-C/SS/DAA,I-	1,2,6	15

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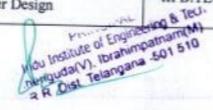
Website: https://siiet.ac.in/

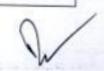
Department of Computer Science and Engineering Faculty Workload

AY:2021-22

II-SEM

	934 800 65°			II-SENI		
S.No	Name of the staff	Name of the Subject / Lab	Year /Course/Branch	No. of hours/week	Total no. of hrs./week	
1	Dr. G.Narayana	1.Compiler Design	III B.TECH CSE-B	5	Man Heek	
250	SPERMENT BETTER THE TOTAL	2.Compiler Design Lab	III B.TECH CSE-B	6	11	
2	Dr.C.Tirumalai Selvan	1.Java Programming	II B.TECH CSE-B	5		
		2. Java Programming Lab	II B.TECH CSE-B	6	11	
3	Dr.L.Kartheesan	1.Operating Systems	II B.TECH CSE-A	5		
			2.Operating Systems Lab	II B.TECH CSE-A	6	11
4	R Yadgiri Rao	1.Machine Learning	III B.TECH CSE-A	5		
	Burran	2.Machine Learning Lab	III B.TECH CSE-A	6	11	
5	Mr.K.Veera Kishore	Fundamentals of Internet of Things	III B.TECH CSE-C	5		
_		2.Project Work	IV B.TECH CSE-A	14	19	
		1.Compiler Design	III B.TECH CSE-A	5		
6	Mrs.Y.Sunitha	2.Compiler Design Lab	III B.TECH CSE-A	6		
	The state of the s	3.Compiler Design	III B.TECH CSE-C	5	22	

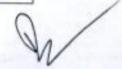




		Database Management Systems	II B.TECH CSE-C	5	
		2.Database Management Systems Lab	II B.TECH CSE-C	6	
		1.Distributed Systems	IV B.TECH CSE-C	5	
12	Mrs C H Sai Vijaya	2.Compiler Design Lab	III B.TECH CSE-B	6	17
		3. Formal Languages & Automata Theory	II B.TECH CSE- AI&ML	5	
		Database Management Systems	II B.TECH CSE-A	5	
13	Mrs S.Akhila	Database Management Systems Lab	II B.TECH CSE-A	6	17
		Software Testing Methodologies Lab	III B.TECH CSE-B	6	
		Software Testing Methodologies	III B.TECH CSE-B	6	
14	Mrs.D.Uma	2. Software Testing Methodologies Lab	III B.TECH CSE-B	6	-
		Software Testing Methodologies Lab	III B.TECH CSE-A	6	21
		4. Software Engineering	II B.TECH CSE- AI&ML	5	
15	Mrs.B.Rajeshwari	1.Design & Analysis of Algorithms	III B.TECH CSE-C	5	11
		Database Management Systems Lab	II B.TECH CSE-B	6	1
16	Mrs.M.Rajyalakshmi	1.Project	IV B.TECH CSE-B	14	14
17	Mr.P.Babu	1. Project	IV B.TECH CSE-A	14	14
18	Mr.P.Dinakar	Database Management Systems Lab	II B.TECH CSE-A	6	6

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Post Telagogog 300



33	A Sudha	1. Project	IV B.TECH CSE-B	14	1 14	
34	A Vijay Kumar	1.Machine Learning	IV B.TECH ECE-A	5	14	
	To July reaman	2. Machine Learning	IV B.TECH ECE -C	5	10	
25	S V Manmadha	1. Machine Learning Lab	III B.TECH CSE-A	6	12	
35	K Manmadha	2.Software Testing Methodolgies Lab	III B.TECH CSE-C	6	12	
36	G Swapna	1.Compiler Design Lab	III B.TECH CSE-C	6	20	
TOTAL .	or or confine	2.Project	IV B.TECH CSE-A	14	- 20	
37	R Sravanthi	Programming For ProblemSolving Lab	I B.TECH CSE-C	6	12	
	K Sravanini	2. Programming For ProblemSolving Lab	I B.TECH CSE-CS	6	12	
		1.Java Programming	II B.TECH CSE-A	5	_	
38	8 N Shilpa	N Shilpa 2. Java Prog	2. Java Programming Lab	II B.TECH CSE-A	6	22
		3.Java Programming	II B.TECH CSE-CS	5	- 22	
		4. Java Programming Lab	II B.TECH CSE-CS	6		

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Course/Subject Name	
Prepared by	
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Class	
nstitute V/M	
Department V / M /PEO	
POs /PSOs	
Course Syllabus with Structure	
Course Outcomes (CO)	
Mapping CO with PO/PSO; Course with PO/PSO with Justification	
Academic Calendar	
Time table - highlighting your course periods including tutorial	
Lesson plan with number of hours/periods, TA/TM, Text/Reference book	
Gap within the syllabus - mapping to CO, PO/PSO	
Gaps beyond the syllabus - Mapping to PO/PSO	
Gaps addressed by a resourse person - document	1
Gaps addressed by any other teaching aid/methodology	
Web references	
Lecture notes	
List of Power point presentations / Videos	
CD with PPT/Videos	
University Question papers	
Internal Question papers, Key with CO and BT	
Assignment Question papers mapped with CO and BT	
Scheme of evaluation with CO and BT mapping	
Tutorial topics with evidence	
Result Analysis to identify weak and advanced learners - 3 times in a semester	
Result Analysis at the end of the course	
Remidial class for weak students - schedule and evidences	nle o
Advance Learners- Engagement documentation	
Course Assessment (Plan & Execution)	
CO, PO/PSO attainment sheets	
Observation for not attaining CO or for improvement	
Plan of action to improve CO attainment next time	
Attendance register (Theory/Tutorial/Remidial) - Teacher/Course delivery record; Continuous evaluation	
Course file (Digital form)	



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Department of Computer Science and Engineering 2021-22; 2nd Semester

Course Outcomes

Course: Software Testing Methodologies (C324) Class: III - II SEM - C - Section

After completing this course the student will be able to:

- C324.1 Recognize the importance, purpose of testing and its applications in software development life cycle. (Knowledge)
- C324.2 List transaction flows ,transaction flow techniques and implementation comments in software testing (Analysis)
- C324.3 Compare domain testing and path testing and explains various domain techniques. (Analysis)
- C324.4 Design reduction procedure and its applications, lists regular expressions and data flow anomaly detection. (Synthesis)
- C324.5 Design and implement state graph, state testing, good state graph, bad state graph and their testability tips. (Synthesis)
- C324.6 Describe graph Matrices, matrix properties and node reduction algorithm.

 (Knowledge)

Mapping of course outcomes with program outcomes:

High -	3		Me	edium	-2		Lo	w-1						
PO/PSO/ CO	P01	PO2	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012	PS01	PS02
C324.1	3	1	2	2	2		-	-	1	*			1	
C324.2	1	2	2	1	2			-	1				2	
C324.3	1	2	1	2									1	1
C324.4	2	1	1	1	×.									1
C324.5	1	2	1	1			. *		1			200	5.4.1	-
C324.6	3	2	1	2	tox 5		70		2		- *			2
C324	1.83	1.67	1.33	1.5	2				1.25				1.3	1.3

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Department of Computer Science and Engineering 2021-22; 2nd Semester

CO - PO / PSO Mapping Justification

Course: Software Testing Methodologies (C324) Class: III - II SEM - C - Section

PROGRAMME OUTCOMES (POs):

- PO1 Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- PO2 Problem analysis: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- PO3 Design/development of solutions: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
- PO4 Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
- PO5 Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
- PO7 Environment and sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- PO9 Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
- PO12 Life-long learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAM SPECIFIC OUTCOMES(PSOs):

- PSO1 Professional Skills: The ability to implement computer programs of varying complexity in the areas related to web design, cloud computing and networking.
- PSO2 Problem-Solving Skills: The ability to develop quality products using open ended programming environment.

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Department of Computer Science and Engineering 2021-22; 2nd Semester CO-PO Mapping Justification

C324.1 Recognize the importance, purpose of testing and its applications in software development life cycle. (Knowledge)

William .	Justification
PO1	Apply the knowledge of testing .
PO2	To identify the types of testing and its applications.
PO3	Design solutions for the software development life cycle.
P04	Use research-based knowledge purpose of testing and its applications.
PO5	Appropriate techniques are used to classify the bugs into different categories
P09	Effectively as an individual work for the testing and understand the process of testing.
PSO1	To implement computer program consequences and understand the importance of bugs.

C324.2 List transaction flows ,transaction flow techniques and implementation comments in software testing (Analysis)

30/4/4	Justification
PO1	Apply the knowledge of transaction techniques.
PO2	To identify the types of transaction flow techniques.
PO3	Design solutions to identify the complications in a transaction flow testing method and anomalies in data flow testing
P04	Use research-based knowledge of comments in software testing.
PO5	Apply appropriate techniques for data flow anomaly state graphs and control flow graphs and represent the state of the data objects.
P09	Function effectively to analyse various strategies of data flow testing.
PSO1	To implement software testing for transaction flow testing and data flow testing.

C324.3 Compare domain testing and path testing and explains various domain techniques. (Analysis)

19275	Justification
P01	Apply the knowledge for concept of domain testing
P02	Identify the ugly and nice domains.
P03	Design the domain testing strategy for different dimension domains.
P04	Use research methods various domain techniques.
PSO1	Ability to implement the mathematical laws (distributive, associative, commutative etc)
PSO2	To develop the control flow graph and identify the path products, path sums and path expressions.

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C324.4 Design reduction procedure and its applications, lists regular expressions and data flow anomaly detection. (Synthesis)

1	Justification					
P01	Apply reduction procedure algorithm to a control flow graph and simplify it into a single path expression					
P02	Identify the probability of paths and understand the need for finding the probabilities.					
P03	Design complimentary operations such as PUSH / POP or GET / RETURN are interpreted in a flow graph.					
P04	Use research methods data flow anomaly detection.					
PS02	The ability to calculate mean processing time of a routine of a given flow graph.					

C324.5Design and implement state graph, state testing, good state graph, bad state graph and their testability tips. (Synthesis)

	Justification	12
PO1	Apply the knowledge for graphical representation of state graphs.	1
PO2	Analyse the Software implementation of sate graphs	15.77
PO3	Design the state graph for good state graph and state graph.	2779
PO4	Use research methods implement state graph.	

C324.6 Describe graph Matrices, matrix properties and node reduction algorithm.

(Knowledge)

	Justification
PO1	Apply the Knowledge of graph matrices, understanding testing theory
PO2	Implementation of node-reduction algorithms.
PO3	Design the graph matrices for node reduction algorithm.
P04	Use research methods implement graph matrices and node reduction algorithm.

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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY Department of Electronics and Communication Engineering



2021-22; 2nd semester COs and Mapping with PO/PSO

Course: Digital Signal Processing (C322)

Class: III ECE-A&B

Course Outcomes

After completing this course the students will be able to:

C322. 1 Analyze the LTI system characteristics and Multirate signal processing. (Analysis)

C322. 2 Compare the inter-relationship between DFT and various transforms. (Evaluation)

C322.3 Design IIR digital filters for a given specification (Synthesis)

C322.4 Design FIR digital filters for a given specification. (Synthesis)

C322.5 Analyze Z -transform analysis on signals and systems. (Analysis)

C322.6 Interpret the significance of various filter structures and effects of round off errors.

(Applications)

Mapping of course outcomes with program outcomes:

High -3 Medium -2

Low-1

PO / CO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
C322. 1	3	3	1	2	2	1	-	-	1			
C322. 2	3	3	3	1	2	2	1	22	1	-	28	<u> </u>
C322. 3	3	3	3	1	2	2	1	-	1	*	•0	-
C322. 4	3	3	3	1	2	2	1	571	1		100	5
C322. 5	3	1	-	1	2	2	æ	-	1	-	-0	-
C322. 6	3	1		172	1	15	-	-	1	6	70	-
C322	3	3	3	1	1.83	2.2	1	25	1	-	20	12

Mr. T Naresh Asst. Prof. /ECE





SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY Department of Electronics and Communication Engineering



2021-22: 2nd semester CO- PO/PSO Mapping - Justification

Course: Digital Signal Processing (C322) Class: III ECE-A, B and C

PO1.	ENGINEERING KNOWLEDGE: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
PO2.	PROBLEM ANALYSIS: Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
PO3.	DESIGN/DEVELOPMENT OF SOLUTIONS: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate considerations for the public health and safety, and the cultural, societal, and environmental considerations.
PO4.	CONDUCT INVESTIGATIONS OF COMPLEX PROBLEMS: Use research- based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
PO5	MODERN TOOL USAGE: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
PO6	THE ENGINEER AND SOCIETY: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
PO7	ENVIRONMENT AND SUSTAINABILITY: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of and need for sustainable development.
PO9	INDIVIDUAL AND TEAM WORK: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

JUSTIFICATION

C322. 1 Analyze the LTI system characteristics and Multirate signal processing.

Apply the knowledge of mathematics, science and engineering fundamentals to analyze the LTI system. (Level 3)
Analyze the problems on LTI system and Multirate Signal Processing. (Level 3)
Developing Solutions for LTI system Characteristics. (Level 1)
Analyzing and interpretation on LTI system Characteristics. (Level 2)
MATLAB tool used to predict and model the LTI system. (Level 2)
The responsibilities of LTI system towards the society (Level 1)
LTI signals and systems with MATLAB Tool. (Level 1)



C322. 2 Compare the inter-relationship between DFT and various transforms.

PO1	Apply the knowledge of mathematics, science and engineering fundamentals to compare the DFT and various transforms (Level 3)
PO2	Analyze the problems on DFT and various transforms. (Level 3)
PO3	Develop a Solutions for DFT and various transforms. (Level 3)
PO4	Analyzing and interpretation on DFT and various transforms. (Level 1)
PO5	MATLAB tool used to convert DFT to IDFT and Vice versa. (Level 2)
PO6	DFT and IDFT are the powerful tool which enable us to find the spectrum of finite duration signal (Level 2)
PO7	Human speech and hearing of signals are encoded with help of DFT, which is most useful for society. (Level 1)
PO9	DFT and IDFT system can be executed with help of MATLAB Tool. (Level 1)

C322.3 Design IIR digital filters for a given specification.

PO1	Apply the knowledge of mathematics, science and engineering fundamentals to design the IIR digital filters. (Level 3)
PO2	Analyze the problems on IIR digital filters. (Level 3)
PO3	Design and Develop a Solutions for IIR digital filters. (Level 3)
PO4	Analyze and interpretation on IIR digital filers. (Level 1)
PO5	MATLAB tool used to design the IIR digital filters. (Level 2)
PO6	IIR digital filter is used in Telecommunication, which is most important to the society. (Level 2)
PO7	Knowledge of IIR digital filter applied to environment. (Level 1)
PO9	IIR digital filter can be executed with help of MATLAB Tool. (Level 1)

C322.4 Design FIR digital filters for a given specification.

PO1	Apply the knowledge of mathematics, science and engineering fundamentals to design the FIR digital filters. (Level 3)
PO2	Analyze the problems on FIR digital filters. (Level 3)
PO3	Design and Develop a Solutions for FIR digital filters. (Level 3)
PO4	Analyzing and interpretation on FIR digital filers. (Level 1)
PO5	MATLAB tool used to design the FIR digital filters. (Level 2)
PO6	FIR digital filter is used in Telecommunication, which is most important to the society. (Level 2)
PO7	Knowledge of FIR digital filter applied to environment. (Level 1)
PO9	FIR digital filter can be executed with help of MATLAB Tool. (Level 1)



C322.5 Express Z -transform analysis on signals and systems.

PO1	Apply the knowledge of mathematics, science and engineering fundamentals to Z transforms analysis on signals and systems. (Level 3)
PO2	Problems on Z Transforms. (Level 1)
PO4	Interpretation on Z transform on signals and systems. (Level 1)
PO5	MATLAB tool used to interpret the Z transform on signals and systems. (Level 2)
PO6	Z transform is used in digital filter relevant to professional engineering practice. (Level 2)
PO9	Analysis of Z transforms on signals and systems can be done with help of MATLAB Tool. (Level 1)

C322.6 Interpret the significance of various filter structures and effects of round off errors.

PO1	Apply the knowledge of mathematics, science and engineering fundamentals to various filter structures. (Level 3)
PO2	Analyze various filter structures and effects of round off errors. (Level 1)
PO5	MATLAB tool used to analyze the various filter structures and effects of round off errors. (Level 1)
PO9	MATLAB Tool used to analyze the various filter structures and effects of round off errors. (Level 1)

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Department of Computer Science and Engineering 2021-22; 2nd Semester

Course Title	SOFTWARE TESTING METHODOLOGIES
Course Code	CS615PE
Programme	B.Tech
Year & Semester	III-year II-semester
Regulation	R18
Course Faculty	Mrs.E.RUPA, Assistant Professor, CSE

LESSON PLAN

S.NO	Unit	TOPIC	Number of Sessions Planned	Teaching method/Aids	REFERENCE
1	F	Introduction	1	Black Board	TI
2		Purpose of testing	1	Black Board	TI
3		Dichotomies	1	Black Board	Ti
4	1	model for testing	1	Black Board	TI
5		Tutorial1(Dichotomies)	1	Black Board	Ti
6	1	consequences of bugs	1	Black Board	Ti
7		taxonomy of bugs Flow graphs	1	Black Board	TI
8		Path testing Introduction	1	Black Board	Ti
9	1	Basics concepts of path testing	1	Black Board	TI
10		Tutorial2(Basics concepts of path testing)	1	Black Board	Ti
11	6.	Predicates Examples	1.	Black Board	- T1
12		path predicates and achievable paths	- 1	Black Board	Ti
13	200	path sensitizing	1	Black Board	Ti
14		path instrumentation, application of path testing. Tutorial3(path sensitizing)	1	Black Board	TI
15		Transaction Flow Testing Introduction	1	Black Board	T)



16	transaction flows	- 1	Black Board	TI
17	transaction flow testing techniques	1	Black Board	TI
18	Dataflow testing	1	Black Board	TI
19	Tutorial4(transaction flow testing techniques)	1	Black Board	TI
20	Basics of dataflow testing	1	Black Board	TI
21 I	strategies in dataflow testing	1	PPT	T1
22	application of dataflow testing	1	PPT	TI
23	Domain Testing Introduction	1	PPT	TI
24	domains and paths	1	PPT	TI
25	TutorialS(application of dataflow testing)	1	Black Board	TI
26	Nice & ugly domains	1	PPT	T1
27	domain testing	1	PPT	TI
28	domains and interfaces testing	1	PPT	T1
29	domain and interface testing	1	PPT	Tl
30	Tutorial6(domainsand testability)	1-1	Black Board	TI
31	Paths, Path products and Regular expressions	1	Black Board	TI
32 III	path products & path expression	1	Black Board	Tl
33	reductionprocedure,	1	Black Board	TI
34	regular expressions & flow	1	Black Board	TI
35	Tutorial7(regular expressions	1	Black Board	TI
36	& flow anomaly detection) Logic Based Testing	1	Black Board	TI
37	Introduction decision tables, path	1	Black Board	TI
38	kv charts, specifications	1	Black Board	TI
39	State, State Graphs and	1	Black Board	TI
40	Transition testing Introduction Tutorial7(Transition testing)	1	Black Board	TI
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41		good & bad state graphs	- 1	Black Board	TI
42		state testing	1	Black Board	TI
43	IV	Testability tips	1	Black Board	TI
44	1	good & bad state graphs Examples	1	Black Board	TI
45		Tutorial8(State Testing)	1	Black Board	TI
46	- 3	Motivational overview	1	Black Board	TI
47		matrix of graph	1	Black Board	TI
48	v	relations, power of a matrix	1	Black Board	TI
49	9-1	node reduction algorithm	1	Black Board	TI
50	2.4	Tutorial9(node reduction algorithm)	1	Black Board	TI
251	1	building tools	1	Black Board	TI

Text Books:

- 1. Software Testing techniques Baris Beizer, Dreamtech, second edition.
- 2. Software Testing Tools Dr. K. V. K. K. Prasad, Dreamtech.

References:

- 1. The craft of software testing Brian Marick, Pearson Education.
- 2. Software Testing Techniques SPD(Oreille)
- 3. Software Testing in the Real World Edward Kit, Pearson.
- 4. Effective methods of Software Testing, Perry, John Wiley.
- 5. Art of Software Testing Meyers, John Wiley

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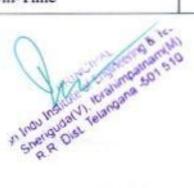
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SRI INDU INSTITUTE OF ENGINEERING & TECHNOLOGY DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

LESSON PLAN

Programm	ne: B.Tech	Academic Year: 202	1-22			
Year: III		Semester: II				
Course Title: Digital Signal Processing		Course Code: EC603PC				
Name of	Faculty: THEDDU NARESH	Number of lectures	per week:5			
No. of Sessions Planned	Topics		Reference	Teaching Method/ Aids		
01	Introduction to Digital Signal Pr Time Signals & Sequences	rocessing: Discrete	T1, R 5	BB		
01	conversion of continuous to discr Frequency	ete signal, Normalized	T1, R 5	BB		
01	Linear Shift Invariant Systems, Causality	Stability, and	TI,R5	BB		
02	linear differential equation to diff Constant Coefficient Difference		T1, R 5	BB		
01	Frequency Domain Representati Signals and Systems		TI,R5	BB		
	Multirate Digital Signal Proce	essing:				
01	Introduction to Multirate samplin			BB		
01	Down sampling and Decimation		T1, R 5	BB		
01	Up sampling and Interpolation		T1, R5	BB		
01	Sampling Rate Conversion		T1, R5	BB		
01	Fourier Series, Fourier Transform	1,	R7	BB		
01	Laplace Transform and Z-Transfo	orm relation	R7, T1	BB		
01	Discrete Fourier series: DFS Representation of Periodic S of Discrete Fourier Series	Sequences, Properties	R7	ВВ		
01	Discrete Fourier Transforms: Pro Linear Convolution of Sequence		R7,R 5	ВВ		
01	Computation of DFT: Over-Lap	Add Method,	T1,R 5	ВВ		
01	Over-Lap Save Method,		T1,R5	BB		
02	Relation between DTFT, DFS Transform.	S, DFT and Z-	T1,R 5	ВВ		
01	Related Problems		T1,R5			
02	Fast Fourier Transforms: Fast (FFT) - Radix-2 Decimation-in-		T1,R 5	ВВ		



02	Decimation-in-Frequency FFT Algorithms, Inverse FFT,	T1, R 5	BB
01	FFT with General Radix-N	T1, R 5	BB
01	Related Problems	T1, R5	BB
01	IIR Digital Filters: Analog filter approximations – Butterworth	R 5, W12	BB
02	Chebyshev, Design of IIR Digital Filters from Analog Filters,	R 5, W21	ВВ
02	Step and Impulse Invariant Techniques,	T1, R 5	BB
02	Bilinear Transformation Method,	T1, R5	BB
02	Spectral Transformations.	T1, R 5	BB
02	Related problems-1	T1, R5	BB
01	Related problems-2	T1, R 5	BB
02	Characteristics of FIR Digital Filters	R 5, W13	
02	Frequency Response	T1, R5	BB
02	Design of FIR Filters: Fourier Method	T1, R 5	ВВ
02	Digital Filters using Window Techniques,	R 5, W7,8	BB
01	Frequency Sampling Technique,	R 5, W12	BB
01	Comparison of IIR & FIR filters	R7	BB
02	Related Problems-1	R 5, W8,9	BB
01	Related Problems-2	T1, R 5	BB
01	Related Problems-3	T1, R 5	BB
02	Realization of Digital Filters: Applications of Z – Transforms	R 5, R7	BB
01	Solution of Difference Equations of Digital Filters,	R 5, R7	ВВ
02	System Function, Stability Criterion.	R 5,W8	BB
02	Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.	R 5,W9	BB

R 5, W

	Finite Word Length Effects:		BB
1	Limit cycles	T1, R5	BB
1	Overflow Oscillations	T1, R 5	BB
2	Round-off Noise in IIR Digital Filters	T1,R5	BB
2	Computational Output Round Off Noise	TI,RS	BB
1	Methods to Prevent Overflow	T1, R 5	ВВ
t	Trade Off Between Round Off and Overflow Noise	T1, R 5	ВВ
1	Measurement of Coefficient Quantization Effects through Pole-Zero Movement	T1, R 5	BB
1	Dead Band Effects	T1, R 5	BB
1	Related Problems – 1	T1, R 5	BB
1	Related Problems – 2	T1, R 5	BB

Text Books:

- Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
- Discrete Time Signal Processing A. V. Oppenheim and R.W. Schaffer, PHI, 2009
- Fundamentals of Digital Signal Processing Loney Ludeman, John Wiley, 2009

Reference Books:

- Digital Signal Processing Fundamentals and Applications Li Tan, Elsevier, 2008
- Fundamentals of Digital Signal Processing using MATLAB Robert J. Schilling, Sandra L. Harris, Thomson, 2007
- Digital Signal Processing S.Salivahanan, A. Vallavaraj and C. Gnanapriya, TMH, 2009
- Discrete Systems and Digital Signal Processing with MATLAB Taan S. EIAli, CRC press, 2009.
- Digital Signal Processing: P.RAMESH BABU,3nd Edition, 2006.
- Digital Signal Processing A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009.
- Digital Signal Processing Nagoor Khani, TMG, 2012



Web References for Digital Signal Processing:

w-1. https://www.profit-forexsignals.com/ https://ocw.mit.edu/resources/res-6-008-digital-signal-processing-spring-2011/ w-2. w-3. https://www.journals.elsevier.com/digital-signal-processing/ https://nptel.ac.in/courses/117102060/ w-4. https://www.sciencedirect.com/journal/digital-signal-processing w-5. w-6. http://www.micromodeler.com/articles/IntroductionToDSP/FIR.jsp w-7. https://www.mikroe.com/ebooks/digital-filter-design/finite-impulse-response-firfilter-design-methods https://engineering.purdue.edu/~bouman/ece637/notes/pdf/Filters.pdf w-8. https://www.minidsp.com/applications/dsp-basics/fir-vs-iir-filtering w-9 http://www.geocities.ws/gorsnehal/fwldsp.pdf w-10. w-11. http://studentsfocus.com/notes/anna university/IT/5SEM/IT6502%20-%20DSP/2marks 16marks/QB104645 2013 regulation.pdf https://www.safaribooksonline.com/library/view/signal-processingw-12. and/9780470710265/9780470710265c08.xhtml w-13. http://users.ece.utexas.edu/~adnan/comm/chang-bliss-sqnr-94.pdf https://www.hindawi.com/journals/vlsi/2014/217495/ w-14. https://www.scribd.com/document/325589591/Unit-v-Finite-Word-Lengthw-15. Effects-in-Digital-Filters https://www.safaribooksonline.com/library/view/signal-processingw-16. and/9780470710265/9780470710265c08.xhtml https://www.intechopen.com/books/applications-of-digital-signal-processing w-17. w-18. https://www.quora.com/What-are-the-applications-of-digital-signal-processing https://en.wikipedia.org/wiki/Digital signal processing w-19. https://www.engineering.com/ProductShowcase/Books/PracticalApplicationsDigi w-20. talSignalProcessing.aspx

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