

**FIRST MEETING OF BOARD OF STUDIES
(ELECTRICAL AND ELECTRONICS ENGINEERING)
Held on 30.09.2022 at 11:00 AM**

Venue: Board Room, Sri Indu Institute of Engineering and Technology (SIET), Autonomous Building.

Board of Studies Committee (BOS) : ELECTRICAL AND ELECTRONICS ENGINEERING

Members Present

S. No.	Name	Designation	Position
1	Mrs. S. Nischala	Associate Professor & HOD, Department of EEE, SIET.	Chairperson
2	Prof. R. Yadagiri Rao	Associate Professor & HOD – H&S Department, SIET.	Member
3	Dr. K. H. Phani Sree	Associate Professor in EEE, JNTUH UCEH.	JNTUH Nominee
4	Dr. T. Anil Kumar	Associate Professor & HOD Anurag University, Hyderabad.	Subject Expert
5	Dr. T. Chandra Shekar	Associate Professor, Maturi Venkata Subba Rao Engineering College, Hyderabad.	Subject Expert
6	Mr. Gujjeti Shiva	Sr. Engineer, Thermo cables Limited.	Industry Expert
7	Mr. K. Rajashekhar	Assistant Professor, SIET.	Member
8	Mr. G. Sandeep Rao	Assistant Professor, SIET.	Member
9	Mr. Gautham Chakilam	Head, Project Monitoring Office, Apollo Med Skills Ltd.	Alumni nominated by the Principal

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Agenda:

- 1.1 Welcome Address by Principal
- 1.2 Composition of Board of Studies (BOS)
- 1.3 Discussion and Approval of the Academic Regulations for the B.Tech I year and II year I & II Semesters.
- 1.4 Discussion and Approval of the Course Structure of B.Tech I year and II year I & II Semesters.
- 1.5 Discussion and Approval of the Detailed Syllabi of B.Tech:
 - i. Basic Electrical Engineering (I Year I-Semester for CSE, CSE (CS) and CSE (DS) & I Year II-Semester for ECE, CSE (AI&ML), CSE (IOT) and AI&DS).
 - ii. Basic Electrical Engineering Laboratory (I Year I-Semester for CSE, CSE (CS) and CSE (DS) & I Year II-Semester for ECE, CSE (AI&ML), CSE (IOT) and AI&DS).
 - iii. Network Analysis and Synthesis (II Year I-Semester for ECE).
 - iv. Basic Electrical and Electronics Engineering (II Year II-Semester for Civil Engineering).
 - v. Basic Electrical and Electronics Engineering Laboratory (II Year II-Semester for Civil Engineering).
- 1.6 Finalization of text books and reference books.
- 1.7 Any other suggestions / advice to the department.
- 1.8 Any other point by the permission of chair.

MINUTES OF MEETING


In the first meeting of BOS in Electrical and Electronics Engineering Department of Sri Indu Institute of Engineering and Technology (Autonomous), the Principal, SIIET welcomed all the members of BOS in Electrical and Electronics Engineering. He informed the members that due to the outstanding performance of the college in the previous years, UGC conferred Autonomous status on this college from the academic year 2022-2023.

The Chairperson of Board of Studies (HOD of EEE Department) has explained that the first meeting of Board of Studies is convened to approve and finalize BR22 Regulations of SIIET (Autonomous) in which all the Rules, Academic Regulations, Course Structure for B.Tech I year and II year I & II Semesters of UG Courses along with the Syllabus of B.Tech. Courses. She said that the draft


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academic regulations, schemes of instruction and syllabi were prepared by the HODs in consultation with their senior staff members, as per the guidelines set out by JNTUH for consideration of the members of the BOS. She urged the members to deliberate on these draft proposals and approve them with corrections and modifications, if any.

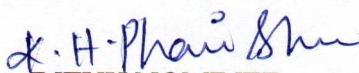
The Chairman, BOS, thanked all the members for being present, and for their co-operation in approving all the items of the Agenda.




CHAIRPERSON
(Mrs. S. Nischala)




MEMBER
(Prof. R. Yadagiri Rao)




JNTUH NOMINEE
(Dr. K. H. Phani Sree)




SUBJECT EXPERT
(Dr. T. Anil Kumar)



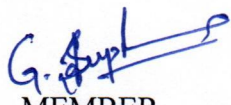
SUBJECT EXPERT
(Dr. T. Chandra Shekar)



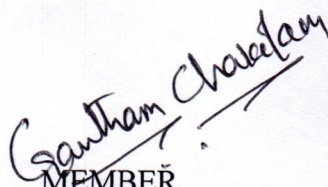
INDUSTRY EXPERT
(Mr. Gujjeti Shiva)



MEMBER
(Mr. K. Rajashekhar)



MEMBER
(Mr. G. Sandeep Rao)



MEMBER
(Mr. Gautham Chakilam)